

PRODUCT DESCRIPTION

EuroProt+ Smart Line S24

IED-EP+S/S24

FEEDER MANAGEMENT RELAY



EUROPROT+ SMART LINE S24 SERIES FEEDER MANAGEMENT RELAY

OVERVIEW

The **IED EP+ S24** Series is member of the **EuroProt+** numerical protection relay, made by Protecta Co. Ltd. The **EuroProt+** type complex protection in respect of hardware and software is a modular device. The modules are assembled and configured according to the requirements, and then the software determines the functions. The **IED EP+ S24** Series is contained a special selection of the EuroProt+ modules, bearing in mind the cost effective realization. The **IED EP+ S24** Series is divided into several different variants corresponding to the scope of application.

The **IED EP+ S24** Series can be used as the main or backup protection of overhead lines and cable networks.

GENERAL FEATURES

- Native IEC 61850 IED with Edition 1 & 2 compatibility
- Standard module layouts with options
- 24 HP wide rack size (height: 3U)
- The pre-defined factory configuration can be customized to the user's specification with the powerful EuroCAP tool
- Flexible protection and control functionality to meet special customer requirements
- Different HMI Types: Advanced HMI with color touchscreen and black-and-white display with 4 tactile push buttons. An embedded web server and extended measuring, control and monitoring functions are also available for both types
- User configurable LCD user screens, which can display SLDs (Single Line Diagrams) with switchgear position indication and control as well as setting values, measurement values, event and fault information (timestamp, function block, fault phase, fault current...)
- 8 setting groups available as default. The number of setting groups can be up to 20 as user's requirement
- Enhanced breaker monitoring and control
- High capacity disturbance recorder (DRE) and event logging in non-volatile memory:
- o DRE can store more than 64 records
- Each DRE recording can be configured up to 32 analogue and 64 digital signal channels with duration up to 10s and sampling rate up to 2kHz
- Event recorder can store more than 10,000 events
- The records can be read out from IED in the standard COMTRADE file format (IEEE Std C37.111) via exist communication connection (such as IEC61850) or even examined on-line. Every single record stored in 3 files with the same name and the following extensions: .dat, .cfg, .inf



- Mounting methods: Flush mounting, Din rail mounting
- Wide range of communication protocols:

0 0

- Ethernet-based communication protocol: IEC61850, DNP3.0 TCP, IEC60870-5-104, Modbus TCP
- Serial communication protocol: DNP3.0, IEC60870-5-101, IEC60870-5-103, MODBUS, SPA
- Legacy network based protocols via 100Base-FX and 10/100Base-TX (RJ45)
- Optional communication ports: Fiber Ethernet (MM/ST, SM/FC), RJ45, Serial POF, Serial glass fiber, RS-485/422
- Handling several communication protocols simultaneously
- Built-in self-monitoring to detect internal hardware or software errors
- Time synchronization protocol: NTP/SNTP, Minute pulse, Legacy protocol, IRIG-B
- Integrated advanced cyber security Conformity with the Cyber Security requirements in accordance with NERC-CIP, IEEE 1686, BDEW Whitepaper and IEC 62351-8 standard and recommendation. Passwords are required when logging into the device for: access, control, setting, manage,...

APPLICATION

The **IED-EP+ S24** Series is available in four predefined standard configurations to suit the most common feeder management application.

- Variant 0 serves as a simple bay control unit.
- **Variant 1** is configured for non-directional overcurrent protection relay.
- Variant 2 is configured for directional overcurrent protection relay.
- Variant 6 is dedicated for those application where is only voltage and frequency based protection functions are required.



SCOPE OF APPLICATION

- Overexcitation protection
- Synchorocheck
- Undervoltage protection
- Directional overpower protection
- Directional underpower protection
- Undercurrent protection
- Negative overcurrent protection
- Negative overvoltage protection
- Motor start-up supervision
- Thermal protection
- Breaker failure protection
- Instantaneous overcurrent protection
- Residual instantaneous overcurrent protection
- Phase overcurrent protection
- Capacitor unbalance protection
- Residual time overcurrent protection
- Voltage dependent overcurrent protection
- Definite time overvoltage protection
- Capacitor overvoltage protection
- Residual overvoltage protection
- Current transformer supervision
- Voltage transformer supervision
- Three-phase directional overcurrent protection
- Residual directional overcurrent protection
- Inrush detection
- Trip circuit supervision
- Vector jump protection
- Auto-reclose
- Overfrequency protection
- Underfrequency protection
- Rate of change of frequency protection
- Lockout trip logic function
- Restricted earth-fault protection
- Switch-onto-fault
- Programmable interlocking schemes

EUROCAP CONFIGURATION TOOL

The EuroCAP configuration tool, which is available free of charge, offers a user-friendly and flexible application for protection, control and measurement functions to ensure that the IED-EP+ devices are fully customisable.

- HW configuration
- View the exciting hardware configuration of the IED including card information and slot position
- Modify (add or change) certain HW modules
- Digital and analogue I/O signal definition



- Logic editor
 - Create/manage logical sheets
 - Factory pre-configured logical schemes to speed up the commissioning process



- Communication configurator
- Set up IEC 61850, 101-104, 103, DNP3 communication protocols
- Configure dataset, report and goose control block properties for IEC 61850 horizontal and vertical communication
- GOOSE configuration between IEDs



- LCD configurator (available with color TFT displays)
 - Create/modify user screens with Single Line Diagrams, measuring or status values
 - Icon library for effective configuration Own, user-defined symbols can be created as well

No for high Yes largery Mil D al E & B - S & - S & - N & - S - N E S T S E S - Spectre



Feedback documentation

Automatic documentation of the configured IED, which can contain the actual connection assignment, on-line measurements, all recorded event channels, all recorded disturbance channels, LED assignment, Logical sheets and the relevant communication settings and collect the protection, control and monitoring parameters.

Offline Parameter Set Editor

- View, set, compare and save the setting of the IED parameters
- Import existing parameter settings into the Offline parameter set editor from the IED
- Import/Export parameters in xlsx format
- Generate and save parameters in RIO/XRIO format for relay tester

PROTECTION & CONTROL FUNCTIONS

ф Ф Ф Ф Ф Ф

The **IED EP+ S24** Series configuration measures three phase currents, the residual current component and additionally three phase voltages and the busbar voltage. These measurements allow, in addition to the current- or voltage-based functions, directionality extension of the configured phase and residual overcurrent functions. It is intended to protect overhead line or cable networks. The choice of the functions is extended with the automatic reclosing function and synchrocheck. The configuration is designed to meet the requirements of a medium or high voltage field unit. Based on the voltage measurement also the frequency is evaluated to realize frequency-based protection functions. The configured protection functions are listed in the table below.

| PROTECTION & CONTROL FUNCTIONS | IEC | ANSI | *Inst. | Var. 0 | Var. 1 | Var. 2 | Var. 6 |
|--|------------------------|----------|--------|--------------|--------------|--------------|--------------|
| Circuit breaker control (included interlocking function) | | | | √ | √ | √ | \checkmark |
| Disconnector control (included interlocking function) | | | | \checkmark | ~ | ~ | \checkmark |
| Overexcitation protection | V/Hz | 24 | 1 | | | \checkmark | |
| Synchrocheck | SYN | 25 | 1 | | | √ | \checkmark |
| Definite time undervoltage protection | U <, U << | 27 | 4 | | | \checkmark | \checkmark |
| Directional overpower protection | P> | 32 | 1 | | | \checkmark | |
| Directional underpower protection | P< | 37 | 1 | | | √ | |
| Loss-of-load (undercurrent) protection | l< | 37 | 1 | | \checkmark | \checkmark | |
| Negative sequence overcurrent protection | 12 > | 46 | 1 | | \checkmark | \checkmark | |
| Broken conductor protection | | 46BC | 1 | | \checkmark | \checkmark | |
| Negative sequence definite time overvoltage protection | U2 > | 47 | 1 | | | \checkmark | \checkmark |
| Motor startup supervision | Istart/I(t) | 48/66 | 1 | | \checkmark | \checkmark | |
| Thermal protection | Τ> | 49 | 1 | | \checkmark | \checkmark | |
| Three-phase instantaneous overcurrent protection | >>> | 50 | 1 | | \checkmark | \checkmark | |
| Breaker failure protection | CBFP | 50BF | 1 | | √ | √ | |
| Residual instantaneous overcurrent protection | lo >>> | 50N/50Ns | 1 | | \checkmark | \checkmark | |
| Three-phase time overcurrent protection | >, >> | 51 | 3 | | √ | √ | |
| Capacitor unbalance protection | | 51C | Op. | | \checkmark | \checkmark | |
| Residual time overcurrent protection | lo >, lo >> | 51N/51Ns | 3 | | √ | √ | |
| Voltage dependent overcurrent protection | I> U< | 51V | 1 | | | \checkmark | |
| Definite time overvoltage protection | U >, U >> | 59 | 4 | | | √ | \checkmark |
| Capacitor overvoltage protection | | 59C | Op. | | \checkmark | \checkmark | |
| Residual overvoltage protection | Uo >, Uo >> | 59N | 4 | | | √ | \checkmark |
| Current transformer supervision | | 60 | 1 | | \checkmark | \checkmark | |
| Voltage transformer supervision | | 60 | 1 | | | √ | \checkmark |
| Directional three-phase overcurrent protection | I Dir >, I Dir >> | 67 | 4 | | | \checkmark | |
| Directional residual time overcurrent protection | lo Dir >, lo Dir >> | 67N/67Ns | 4 | | | √ | |
| Inrush detection | l2h > | 68 | 1 | | √ | √ | |
| Trip circuit supervision | | 74 | 1 | \checkmark | \checkmark | \checkmark | \checkmark |
| Vector jump protection | ΔφU > | 78 | 1 | | | \checkmark | |
| Auto-reclose | $0 \rightarrow 1$ | 79 | 1 | | \checkmark | \checkmark | |
| Overfrequency protection | f >, f >> | 810 | 4 | | | \checkmark | \checkmark |
| Underfrequency protection | f <, f << | 81U | 4 | | | \checkmark | \checkmark |
| Rate of change of frequency protection | df/dt | 81R | 2 | | | \checkmark | \checkmark |
| Lockout trip logic function | | 86/94 | 1 | \checkmark | \checkmark | \checkmark | \checkmark |
| Restricted earth-fault protection | REF | 87N | Op. | | | \checkmark | |
| Switch-onto-fault | | SOFT | 1 | | \checkmark | \checkmark | |

*The Inst. column contains the numbers of the pre-configured function blocks in the factory configuration. These numbers may be different in order to meet the user's requirements.

Circuit breaker control function block (CB1Pol)

The Circuit breaker control function block can be used to integrate the circuit breaker control of the EuroProt+ device into the station control system and to apply active scheme screens of the local LCD of the device. Up to 32 Circuit breaker control function blocks can be configured.

The Circuit breaker control function block receives remote commands from the SCADA system and local commands from the local LCD of the device, performs the prescribed checking and transmits the commands to the circuit breaker. It processes the status signals received from the circuit breaker and offers them to the status display of the local LCD and to the SCADA system.

Main features:

- Local (LCD of the device) and Remote (SCADA) operation modes can be enabled or disabled individually.
- The signals and commands of the synchro check/synchro switch function block can be integrated into the operation of the function block.
- Interlocking functions can be programmed by the user applying the inputs "EnaOff" (enabled trip command) and "EnaOn" (enabled close command), using the graphic equation editor.
- Programmed conditions can be used to temporarily disable the operation of the function block using the graphic equation editor.
- The function block supports the control models prescribed by the IEC 61850 standard.
- All necessary timing tasks are performed within the function block:
- o Time limitation to execute a command
- o Command pulse duration
- o Filtering the intermediate state of the circuit breaker
- Checking the synchro check and synchro switch times
- Controlling the individual steps of the manual commands
- Sending trip and close commands to the circuit breaker (to be combined with the trip commands of the protection functions and with the close command of the automatic reclosing function; the protection functions and the automatic reclosing function directly give commands to the CB). The combination is made graphically using the graphic equation editor
- Operation counter
- Event reporting

The Circuit breaker control function block has binary input signals. The conditions are defined by the user applying the graphic equation editor. The signals of the circuit breaker control are seen in the binary input status list.

Disconnector control function (DisConn)

The Disconnector control function block can be used to integrate the disconnector or earthing switch control of the

EuroProt+ device into the station control system and to apply active scheme screens of the local LCD of the device. Up to 32 Disconnector control function blocks can be configured.

The disconnector control function block receives remote commands from the SCADA system and local commands from the local LCD of the device, performs the prescribed checking and transmits the commands to the disconnector. It processes the status signals received from the disconnector and offers them to the status display of the local LCD and to the SCADA system.

Main features:

- Local (LCD of the device) and Remote (SCADA) operation modes can be enabled or disabled individually.
- Interlocking functions can be programmed by the user applying the inputs "EnaOff" (enabled trip command) and "EnaOn" (enabled close command), using the graphic equation editor.
- Programmed conditions can be used to temporarily disable the operation of the function block using the graphic equation editor.
- The function block supports the control models prescribed by the IEC 61850 standard.
- All necessary timing tasks are performed within the function block:
 - o Time limitation to execute a command
 - o Command pulse duration
 - o Filtering the intermediate state of the disconnector
 - \circ $\;$ Controlling the individual steps of the manual commands
- Sending trip and close commands to the disconnector
- Operation counter
- Event reporting

The Disconnector control function block has binary input signals. The conditions are defined by the user applying the graphic equation editor. The signals of the disconnector control are seen in the binary input status list.

Overexcitation protection (24)

The overexcitation protection function is applied to protect generators and unit transformers against high flux values causing saturation of the iron cores and consequently high magnetizing currents.

The peak value of the flux increases if the magnitude of the voltage increases, and/or the flux can be high if the duration of a period increases; this means that the frequency of the voltage decreases. That is, the flux is proportional to the peak value of the voltage (or to the RMS value) and inversely proportional to the frequency.

The effect of high flux values is the symmetrical saturation of the iron core of the generator or that of the unit transformer. During saturation, the magnetizing current is high and distorted; high current peaks can be detected. The odd harmonic components of the current are of high magnitude and the RMS



value of the current also increases. The high peak current values generate high dynamic forces, the high RMS value causes overheating. During saturation, the flux leaves the iron core and high eddy currents are generated in the metallic part of the generator or transformer in which normally no current flows, and which is not designed to withstand overheating. The overexcitation protection is designed to prevent this long-term overexcited state

The magnitude can be calculated if at least one positive and one negative peak value have been found, and the function starts if the calculated flux magnitude is above the setting value. Accordingly, the starting delay of the function depends on the frequency: if the frequency is low, more time is needed to reach the opposite peak value. In case of energizing, the time to find the first peak depends on the starting phase angle of the sinusoidal flux. If the voltage is increased continuously by increasing the excitation of the generator, this time delay cannot be measured.

As the heating effect of the distorted current is not directly proportional to the flux value, the applied characteristic is of inverse type (so called IEEE type): If the overexcitation increases, the operating time decreases. To meet the requirements of application, a definite-time characteristic is also offered in this protection function as an alternative.

Overexcitation is a typically symmetrical phenomenon. There are other dedicated protection functions against asymmetry. Accordingly, the processing of a single voltage is sufficient. In a network with isolated start point, the phase voltage is not exactly defined due to the uncertain zero sequence voltage component. Therefore, line-to-line voltages are calculated based on the measured phase voltages, and one of them is assigned to overfluxing protection.

The effective frequency range includes all frequencies where the defined accuracy can be achieved. If the frequency is too small, then the time needed to find the peak values and to calculate the flux increases. In contrast, at high frequencies the accuracy of the detected peak value decreases. The frequency range monitored extends from 10 Hz to 70 Hz. The details are given among the technical data.

Similar to the frequency range, the voltage range is also limited. If the voltage is too small, the voltage measurement becomes inaccurate due to the sampling. In case of high voltage at low frequencies the voltage transformers may also saturate. Accordingly, the frequency range and the voltage range are closely related. The voltage range monitored extends from 10 V to 170 V. The details are given among the technical data.

The flux range is the combination of the voltage range and the frequency range. For overfluxing protection, the effective flux range extends from 0.5 to 1.5 UN/fN

Synchro check, synchro switch (25)

Several problems can occur in the electric power system if the circuit breaker closes and connects two systems operating asynchronously. The high current surge can cause damage in the interconnecting elements, the accelerating forces can overstress the shafts of rotating machines or, at last, the actions taken by the protective system can result in the unwanted separation of parts of the electric power system. To prevent such problems, this function checks whether the systems to be interconnected are operating synchronously. If yes, then the close command is transmitted to the circuit breaker. In case of asynchronous operation, the close command is delayed to wait for the appropriate vector position of the voltage vectors on both sides of the circuit breaker. If the conditions for safe closing cannot be fulfilled within an expected time, then closing is declined.

There are three modes of operation:

- Energizing check:
 - \circ $\,$ Dead bus, live line,
 - \circ $\;$ Live bus, dead line,
 - Any Energizing Case (including Dead bus, dead line).
- Synchro check (Live line, live bus)
- Synchro switch (Live line, live bus)

The function can be started by the switching request signals initiated both the automatic reclosing and the manual closing. The binary input signals are defined by the user, applying the graphic equation editor.

Blocking signal of the function are defined by the user, applying the graphic equation editor. Blocking signal of the voltage transformer supervision function for all voltage sources are defined by the user, applying the graphic equation editor.

Signal to interrupt (cancel) the automatic or the manual switching procedure are defined by the user, applying the graphic equation editor.

Definite time undervoltage protection (27)

The definite time undervoltage protection function measures the RMS values of the fundamental Fourier component of three phase voltages. The Fourier calculation inputs are the sampled values of the three phase voltages (UL1, UL2, UL3), and the outputs are the basic Fourier components of the analyzed voltages (UL1Four, UL2Four, UL3Four). They are not part of the TUV27 function; they belong to the preparatory phase.

The function generates start signals for the phases individually. The general start signal is generated if the voltage is below the preset starting level parameter setting value and above the defined blocking level. The function generates a trip command only if the definite time delay has expired and the parameter selection requires a trip command as well.

The operation mode can be chosen by the type selection parameter. The function can be disabled, and can be set to "1 out of 3", "2 out of 3", and "All".

The overvoltage protection function has a binary input signal, which serves the purpose of disabling the function. The conditions of disabling are defined by the user, applying the graphic equation editor.



The directional over-power protection function can be applied to protect any elements of the electric power system mainly generators if the active and/or reactive power has to be limited.

The inputs of the function are the Fourier basic harmonic components of the three phase currents and those of the three phase voltages. Based on the measured voltages and currents, the block calculates the three-phase activeand reactive power (point S) and compares the P-Q coordinates with the defined characteristics on the power plane. The characteristic is defined as a line laying on the point SS and perpendicular to the direction of SS. The SS point is defined by the "Start power" magnitude and the "Direction angle". The over-power function operates if the angle of the S-SS vector related to the directional line is below 90 degrees and above -90 degrees.

At operation, the "Start power" value is decreased by a hysteresis value.



Directional under-power protection (37)

The directional under-power protection function can be applied mainly to protect any elements of the electric power system, mainly generators, if the active and/or reactive power has to be limited in respect of the allowed minimum power.

The inputs of the function are the Fourier basic harmonic components of the three phase currents and those of the three phase voltages. Based on the measured voltages and currents, the block calculates the three-phase active and reactive power (point S) and compares the P-Q coordinates with the defined characteristics on the power plane. The characteristic is defined as a line laying on the point SS and perpendicular to the direction of SS. The SS point is defined by the "Start power" magnitude and the "Direction angle". The under-power function operates if the angle of the S-SS vector related to the directional line is above 90 degrees or below -90 degrees, i.e. if the point S is on the "Operate" side of the P-Q plane.

At operation, the "Start power" value is increased by a hysteresis value.



Loss-of-load (undercurrent) protection (37)

The loss-of-load (undercurrent) protection function operates when the current decreases below a predetermined value.

This protection function can be applied for fan or pump drives, where the flowing media provides cooling for the motor itself. If this cooling stops, the motor must not remain I operation. In these cases the protection against low load after a given time delay disconnects the motor from the power supply.

It can also stop a motor in case of a failure in a mechanical transmission (e.g. conveyor belt).

A time delay may be required after start of the function to prevent operation during transient of the power systems.

The advantage of this function is its simplicity: no voltage measurement is needed, no power calculations are performed. The operation is based on phase currents only.

Negative sequence overcurrent protection (46)

The negative sequence overcurrent protection function (46) block operates if the negative sequence current is higher than the preset starting value. In the negative sequence overcurrent protection function, definite-time or inverse-time characteristics are implemented, according to IEC or IEEE standards. The function evaluates a single measured current, which is the RMS value of the fundamental Fourier component of the negative sequence current. The characteristics are harmonized with IEC 60255-151, Edition 1.0, 2009-08. The definite (independent) time characteristic has a fixed delaying time when the current is above the starting current Gs previously set as a parameter. The negative phase sequence components calculation is based on the Fourier components of the phase currents.

The binary output status signals of the negative sequence overcurrent protection function are the general starting and the general trip command of the function.

The negative sequence overcurrent protection function has a binary input signal, which serves the purpose of disabling the function. The conditions of disabling are defined by the user, applying the graphic equation editor.

Broken conductor protection (46BC)

The broken conductor protection function can be applied to detect a power lines and cables broken conductor condition or a single-pole breaker malfunction condition.

By measuring the phase current input signals and compares the ratio of negative phase sequence current (I2) to positive



phase sequence current (I1). If the I2/I1 ratio is above the setting limit, the function generates a start signal. It is a necessary precondition of start signal generation that the positive phase sequence current (I1) must be between 6.67% and 100% of the rated current.

The function can be disabled by parameter setting, and by an input signal programmed by the user with the graphic programming tool. The trip command is generated after the defined time delay if trip command is enabled by parameter setting.

Negative sequence definite time overvoltage protection (47)

The definite time negative sequence overvoltage protection function measures three voltages and calculates the negative sequence component. If the negative sequence component is above the level defined by parameter setting, then a start signal is generated. The function generates a start signal. The general start signal is generated if the negative sequence voltage component is above the level defined by parameter setting value. The function generates a trip command only if the time delay has expired and the parameter selection requires a trip command as well.

The function can be disabled by parameter setting or by an external signal, edited by the graphic logic editor.

Motor startup supervision (48/66)

• Monitoring the startup

The available functions of the motor startup supervision provide optimal protection during the startup procedure.

The starting process, which is an extreme stress for the motor, is automatically detected based on the fact that the current is zero before starting (below the set Idle Current parameter), then it increases above that level. During the motor starting process, the duration of which is limited by the Start-up Time parameter, a dedicated binary output signal indicates the startup process. This signal can be applied, for instance, to activate the startup overcurrent protection function, which takes over the protection tasks from the normal overcurrent protection functions.

During the starting time the normal overcurrent protection function is not effective, but the special overcurrent function can operate without any considerable time delay: if the current rises above the increased current setting, the function generates an immediate trip command for the circuit breaker. Based on the starting signal at the end of the successful starting process, the normal overcurrent function is activated again, the setting of which can be below the starting current, providing optimal protection for the motor.

Locked rotor protection:

If the starting process of the motor lasts too long, the motor is subject to a harmful overstress. If the starting current in excess of the motor Start-up Current parameter value can be detected after the defined Start-up Time, the function generates a trip command.

• Operation of the motor startup supervision

As the basic setting, the rated current of the motor must be defined as a percentage of the rated current of the current transformer.

The starting state is recognized by the algorithm if the current changes from zero value to a higher current. This event triggers a timer, which is in "running" state for the starting time set, then it changes to the "time-out" state. The starting time is set by the parameter Start-up Time.

If the current is above the Idle Current limit, then the motor is considered to be in running state.

If the timer defined by the Start-up Time parameter runs out, then the current must be below a level defined by the parameter Start-up Current. Otherwise, it is an indication of prolonged startup time or a locked rotor. In this case, the function generates a signal, which can be applied to interrupt the starting procedure by tripping the circuit breaker.

When the startup timer runs out, another independent timer is started. During the running time of this second timer no restarting is allowed because the repeated increased starting current could cause overheating in the motor. This inhibition timer's designated parameter is the Restart Time.

The restart inhibition time is also started if the starting process is interrupted and the current falls below the Idle Current.

The function counts the subsequent startups within the last hours. This count must not be above the permitted startup numbers, defined by a dedicated parameter. If this parameter is 0 then no limit is considered. The last remaining restart possibility is indicated by an output status signal of the function block.

Thermal protection (49)

Basically, thermal protection measures the three sampled phase currents. RMS values are calculated and the temperature calculation is based on the highest RMS value of the phase currents. The temperature calculation is based on the step-bystep solution of the thermal differential equation. This method yields "over temperature", meaning the temperature above the ambient temperature. Accordingly, the temperature of the protected object is the sum of the calculated "over temperature" and the ambient temperature.

If the calculated temperature (calculated "over temperature" + ambient temperature) is above the threshold values, alarm, trip and restart blocking status signals are generated.

Three-phase instantaneous overcurrent protection (50)

The three-phase instantaneous overcurrent protection function (50) operates immediately if the phase currents are higher than the setting value. The setting value is a parameter, and it can be doubled by graphic programming of the dedicated



input binary signal defined by the user. The function is based on peak value selection or on the RMS values of the Fourier basic harmonic calculation, according to the parameter setting. The fundamental Fourier components are results of an external function block.

Parameter for type selection has selection range of Off, Peak value and Fundamental value. When Fourier calculation is selected then the accuracy of the operation is high, the operation time however is above one period of the network frequency. If the operation is based on peak values then fast sub-cycle operation can be expected, but the transient overreach can be high.

The function generates trip commands without additional time delay if the detected values are above the current setting value. The function generates trip commands for the three phases individually and a general trip command as well.

The instantaneous overcurrent protection function has a binary input signal, which serves the purpose of disabling the function. The conditions of disabling are defined by the user, applying the graphic equation editor.

Breaker failure protection (50BF)

After a protection function generates a trip command, it is expected that the circuit breaker opens and the fault current drops below the pre-defined normal level. If not, then an additional trip command must be generated for all backup circuit breakers to clear the fault. At the same time, if required, a repeated trip command can be generated to the circuit breakers which are a priori expected to open. The breaker failure protection function can be applied to perform this task.

The starting signal of the breaker failure protection function is usually the trip command of any other protection function. Dedicated timer starts at the rising edge of the general start signal for the backup trip command. During the running time of the timer the function optionally monitors the currents, the closed state of the circuit breakers or both, according to the user's choice. The selection is made using an enumerated parameter.

If current supervision is selected by the user then the current limit values must be set correctly. The binary input indicating the status of the circuit breaker has no meaning.

If contact supervision is selected by the user then the current limit values have no meaning. The binary input indicating the status of the circuit breaker must be programmed correctly using the graphic equation editor.

If the parameter selection is "Current/Contact", the current parameters and the status signal must be set correctly. The breaker failure protection function resets only if all conditions for faultless state are fulfilled.

If at the end of the running time of the backup timer the currents do not drop below the pre- defined level, and/or the monitored circuit breaker is still in closed position, then a backup trip command is generated.

The pulse duration of the trip command is not shorter than

the time defined by setting the parameter Pulse length.

The breaker failure protection function can be disabled by setting the enabling parameter to "Off".

Dynamic blocking (inhibition) is possible using the binary input Block. The conditions are to be programmed by the user, using the graphic equation editor.

Residual instantaneous overcurrent protection (50N/50Ns)

The residual instantaneous overcurrent protection function operates immediately if the residual current (3lo) is above the setting value. The setting value is a parameter, and it can be doubled by a dedicated binary input signal defined by the user applying the graphic programming. The function is based on peak value selection or on the RMS values of the Fourier basic harmonic component of the residual current, according to the parameter setting. The fundamental Fourier component calculation is not part of the 50N/50Ns function. Parameter for type selection has selection range of Off, Peak value and Fundamental value.

The function generates a trip commands without additional time delay if the detected values are above the current setting value.

If the relay is equipped with the current transformer module with a sensitive channel (4th channel), the function will be considered as sensitive residual instantaneous overcurrent protection for use in applications where the fault current magnitude may be very low.

The residual instantaneous overcurrent protection function has a binary input signal, which serves the purpose of disabling the function. The conditions of disabling are defined by the user, applying the graphic equation editor.

Three-phase time overcurrent protection (51)

The overcurrent protection function realizes definite time or inverse time characteristics according to IEC or IEEE standards, based on three phase currents. The characteristics are harmonized with IEC 60255-151, Edition 1.0, 2009-08. This function can be applied as main protection for medium-voltage applications or backup or overload protection for high-voltage network elements. The definite (independent) time characteristic has a fixed time delay when the current is above the starting current is previously set as a parameter.

The binary output status signals of the three-phase overcurrent protection function are starting signals of the three phases individually, a general starting signal and a general trip command.

The overcurrent protection function has a binary input signal, which serves the purpose of disabling the function. The conditions of disabling are defined by the user, applying the graphic equation editor.

Capacitor unbalance protection (51C)

The main purpose of the capacitor unbalance protection is



to give an alarm or to disconnect the entire capacitor bank when unbalances across healthy capacitors, adjacent to a failed capacitor, are excessive. Normally not more than 10 % unbalance should be allowed (unbalance limit according to IEC 871-1 Shunt capacitors for a.c. power systems having a rated voltage above 1000V – Part 1: General).

If an externally fused capacitor is disconnected by its fuse, a larger voltage and current change is obtained than if single elements are disconnected by internal fuses.

This kind of protection prevents steady-state overvoltage and accelerated aging of the capacitor elements.

Another function of the unbalance protection is to remove the bank from service for a fault not isolated by a fuse or to protect banks that are not internally or externally fused. Unbalancenprotection is not a replacement for short-circuit protection.

Residual overcurrent protection (51N/51Ns)

The residual delayed overcurrent protection function can realize definite time or inverse time characteristics according to IEC or IEEE standards, based on the RMS value of the fundamental Fourier component of a single measured current, which can be the measured residual current at the neutral point (3lo) or the calculated zero sequence current component. The characteristics are harmonized with IEC 60255-151, Edition 1.0, 2009-08. The definite (independent) time characteristic has a fixed time delay when the current is above the starting current Is previously set as a parameter.

The binary output status signals of the residual overcurrent protection function are the general starting signal and the general trip command if the time delay determined by the characteristics expired.

If the relay is equipped with the current transformer module with a sensitive channel (4th channel), the function will be considered as sensitive residual overcurrent protection (51Ns) for use in applications where the fault current magnitude may be very low.

The residual overcurrent protection function has a binary input signal, which serves the purpose of disabling the function. The conditions of disabling are defined by the user, applying the graphic equation editor.

Voltage dependent overcurrent protection (51V)

When overcurrent protection function is applied and the current in normal operation can be high, related to the lowest fault current then the correct setting is not possible based on current values only. In this case however, if the voltage during fault is considerably below the lowest voltage during operation then the voltage can be applied to distinguish between faulty state and normal operating state. This is the application area of the voltage dependent overcurrent protection function.

The function has two modes of operation, depending on the parameter setting:

Voltage controlled

The overcurrent protection function realizes definite time characteristic based on three phase currents. The operation is restrained or controlled by three phase voltages. The function operates in three phases individually, but the generated general start signal and the general trip command is the OR relationship of the three decisions.

The function can be blocked by a user-defined signal or by the voltage transformer supervision function block, if the measured voltage is not available.

Definite time overvoltage protection (59)

The definite time overvoltage protection function measures three voltages. The measured values of the characteristic quantity are the RMS values of the basic Fourier components of the phase voltages. The Fourier calculation inputs are the sampled values of the three phase voltages (UL1, UL2, UL3), and the outputs are the basic Fourier components of the analyzed voltages (UL1Four, UL2Four, UL3Four). They are not part of the 59 function; they belong to the preparatory phase.

The function generates start signals for the phases individually. The general start signal is generated if the voltage in any of the three measured voltages is above the level defined by parameter setting value. The function generates a trip command only if the definite time delay has expired and the parameter selection requires a trip command as well.

The overvoltage protection function has a binary input signal, which serves the purpose of disabling the function. The conditions of disabling are defined by the user, applying the graphic equation editor.

Capacitor overvoltage protection (59C)

The capacitors on a network in most cases have no dedicated voltage measurement, the voltage transformers on the busbar measure voltage even in disconnected state of the capacitors. To avoid these kinds of problems, this protection function measures the currents in the phases of the capacitor, and calculates the voltages in the phases independently. The warning and trip decision is based on the calculated voltage values.

Residual definite time overvoltage protection (59N)

The residual definite time overvoltage protection function operates according to definite time characteristics, using the RMS values of the fundamental Fourier component of the zero sequence voltage (UN=3Uo). The Fourier calculation inputs are the sampled values of the residual or neutral voltage (UN=3Uo) and the outputs are the RMS value of the basic Fourier components of those.

The function generates start signal if the residual voltage is above the level defined by parameter setting value. The function generates a trip command only if the definite time delay has expired and the parameter selection requires a trip command as well.

Voltage restrained



The residual overvoltage protection function has a binary input signal, which serves the purpose of disabling the function. The conditions of disabling are defined by the user, applying the graphic equation editor.

Current transformer supervision (60)

The current transformer supervision function can be applied to detect unexpected asymmetry in current measurement.

The applied method selects maximum and minimum phase currents (fundamental Fourier components). If the difference between them is above the setting limit, the function generates a start signal. It is a necessary precondition of start signal generation that the maximum of the currents be above 10 % of the rated current and below 150% of the rated current.

The function can be disabled by parameter setting, and by an input signal programmed by theuser with the graphic programming tool.

The failure signal is generated after the defined time delay.

Voltage transformer supervision (60)

The voltage transformer supervision function generates a signal to indicate an error in the voltage transformer secondary circuit. This signal can serve, for example, as a warning, indicating disturbances in the measurement, or it can disable the operation of the distance protection function if appropriate measured voltage signals are not available for a distance decision.

The voltage transformer supervision function is designed to detect faulty asymmetrical states of the voltage transformer circuit caused, for example, by a broken conductor in the secondary circuit. The user has to generate graphic equations for the application of the signal of this voltage transformer supervision function.

The voltage transformer supervision function can be used in three different modes of application:

- Zero sequence detection (for typical applications in systems with grounded neutral): "VT failure" signal is generated if the residual voltage (3Uo) is above the preset voltage value AND the residual current (3Io) is below the preset current value.
- Negative sequence detection (for typical applications in systems with isolated or resonant grounded (Petersen) neutral): "VT failure" signal is generated if the negative sequence voltage component (U2) is above the preset voltage value AND the negative sequence current component (I2) is below the preset current value.
- Special application: "VT failure" signal is generated if the residual voltage (3Uo) is above the preset voltage value AND the residual current (3Io) AND the negative sequence current component (I2) are below the preset current values.

The voltage transformer supervision function can be activated if "Live line" status is detected for at least 200 ms.

This delay avoids mal-operation at line energizing if the poles of the circuit breaker make contact with a time delay. The function is set to be inactive if "Dead line" status is detected.

If the conditions specified by the selected mode of operation are fulfilled (for at least 4 milliseconds) then the voltage transformer supervision function is activated and the operation signal is generated. (When evaluating this time delay, the natural operating time of the applied Fourier algorithm must also be considered.

Three-phase directional overcurrent protection (67)

The directional three-phase overcurrent protection function can be applied on solidly grounded, compensated or isolated networks, where the overcurrent protection must be supplemented with a directional decision.

The direction can be selected as forward or backward. The overcurrent decision can be set also without considering the decision.

The overcurrent decision can be based on current RMS values or on Fourier fundamental harmonic values.

The time overcurrent characteristic can be definite time or several types of standard IEC or ANSI characteristics.

The function can be enabled or disabled by a parameter. The status signal of the VTS (voltage transformer supervision) function can also disable the directional operation.

Residual directional overcurrent protection (67N/67Ns)

The main application area of the directional residual delayed overcurrent protection function is an earth-fault protection.

The inputs of the function are the RMS value of the Fourier basic harmonic components of the zero sequence current (IN=3Io) and those of the zero sequence voltage (UN=3Uo).

The block of the directional decision generates a signal of TRUE value if the U_N=3U₀ sequence voltage zero and the $I_N=3I_0$ zero sequence current are above the limits needed directional for correct decision, and the angle



difference between the vectors is within the preset range. The decision enables the output start and trip signal of an overcurrent protection function block (51N/51Ns). This nondirectional residual overcurrent protection function block is described in a separate document. The directional decision module calculates the phase angle between the residual voltage and the residual current. The reference signal is the residual voltage according to the Figure.

The output of the directional decision module is OK, namely it is TRUE if the phase angle between the residual voltage and the residual current is within the limit range defined by the preset parameter OR if non-directional operation is selected by



the preset parameter (Direction=NonDir).

If the relay is equipped with the current transformer module with a sensitive channel (4th channel), the function will be considered as sensitive residual directional overcurrent protection (67Ns) for use in applications where the fault current magnitude may be very low.

Inrush detection (68)

When an inductive element with an iron core (transformer, reactor, etc.) is energized, high current peak values can be detected. This is caused by the transient asymmetric saturation of the iron core as a nonlinear element in the power network. The sizing of the iron core is usually sufficient to keep the steady state magnetic flux values below the saturation point of the iron core, so the inrush transient slowly dies out. These current peaks depend also on random factors such as the phase angle at energizing. Depending on the shape of the magnetization curve of the iron core, the detected peaks can be several times above the rated current peaks. Additionally, in medium or high voltage networks, where losses and damping are low, the indicated high current values may be sustained at length. The function operates independently using all three phase currents individually, and additionally, a general inrush detection signal is generated if any of the phases detects inrush current.

The function can be disabled by the binary input Disable. This signal is the result of logic equations graphically edited by the user. Using the inrush detection binary signals, other protection functions can be blocked during the transient period so as to avoid the unwanted trip.

Trip circuit supervision (74)

The trip circuit supervision is utilized for checking the integrity of the circuit between the trip coil and the tripping output of the protection device.

This is realized by injecting a small DC current (around 1-5 mA) into the trip circuit. If the circuit is intact, the current flows, causing an active signal to the opto coupler input of the trip contact.

The state of the input is shown on the devices' binary input listing among the other binary inputs, and it can be handled like any other of them (it can be added to the user logic, etc.)

Vector jump protection (78)

The modern electric power systems include an increasing number of small generators (distributed generation system). There can be several events in the network resulting that the small generators get disconnected from the system, and the small generator supplies some consumer only, remaining in the electric "island" (unintended islanding).

If a small generator remains in an island with some consumers, it is highly possible that the balance of the generated and consumed active and reactive power is not fulfilled. This results changing of the frequency and/or voltage, accordingly the voltage vector position of the island is changing, related to that of the disconnected grid. An automatic reclosing of the circuit breaker at an unfavorable vector position can result high currents and serious damages. To prevent these damages a protection is needed to detect the islanding and to disconnect the generator from the island.

One of the protection methods to detect unintended islanding is this vector jump protection function.

Auto-reclose (79)

The automatic reclosing function can realize up to four shots of reclosing. The dead time can be set individually for each reclosing and separately for earth faults and for multi-phase faults. All shots are of three phase reclosing. The starting signal of the cycles can be generated by any combination of the protection functions or external signals of the binary inputs.

The automatic reclosing function is triggered if as a consequence of a fault a protection function generates a trip command to the circuit breaker and the protection function resets because the fault current drops to zero or the circuit breaker's auxiliary contact signals open state. According to the preset parameter values, either of these two conditions starts counting the dead time, at the end of which the automatic reclosing function generates a close command automatically. If the fault still exits or reappears, then within the "Reclaim time" the protection functions picks up again and the subsequent cycle is started. If the fault still exists at the end of the last cycle, the automatic reclosing function trips and generates the signal for final trip. If no pickup is detected within this time, then the automatic reclosing cycle resets and a new fault will start the procedure with the first cycle again.

At the moment of generating the close command, the circuit breaker must be ready for operation, which is signaled via the binary input "CB Ready". The preset parameter value "CB Supervision time" decides how long the automatic reclosing function is allowed to wait at the end of the dead time for this signal. If the signal is not received during this dead time extension, then the automatic reclosing function terminates.

Depending on binary parameter settings, the automatic reclosing function block can accelerate trip commands of the individual reclosing cycles. This function needs userprogrammed graphic equations to generate the accelerated trip command. The automatic reclosing function can be blocked by a binary input. The conditions are defined by the user applying the graphic equation editor.

Over-frequency protection (810)

The deviation of the frequency from the rated system frequency indicates unbalance between the generated power and the load demand. If the available generation is large compared to the consumption by the load connected to the power system, then the system frequency is above the rated value. The over-frequency protection function is usually applied to decrease generation to control the system frequency.



Another possible application is the detection of unintended island operation of distributed generation and some consumers. In the island, there is low probability that the power generated is the same as consumption; accordingly, the detection of high frequency can be one of the indication of island operation.

The over-frequency protection function generates a start signal if at least five measured frequency values are above the preset level. Time delay can also be set.

The function can be enabled/disabled by a parameter.

The over-frequency protection function has a binary input signal. The conditions of the input signal are defined by the user, applying the graphic equation editor. The signal can block the under-frequency protection function.

Underfrequency protection (81U)

The deviation of the frequency from the rated system frequency indicates unbalance between the generated power and the load demand. If the available generation is small compared to the consumption by the load connected to the power system, then the system frequency is below the rated value. The under-frequency protection function is usually applied to increase generation or for load shedding to control the system frequency. Another possible application is the detection of unintended island operation of distributed generation and some consumers. In the island, there is low probability that the power generated is the same as consumption; accordingly, the detection of low frequency can be one of the indications of island operation. Accurate frequency measurement is also the criterion for the synchrocheck and synchro- switch functions.

The under-frequency protection function generates a start signal if at least five measured frequency values are below the setting value. Time delay can also be set.

The function can be enabled/disabled by a parameter.

The under-frequency protection function has a binary input signal. The conditions of the input signal are defined by the user, applying the graphic equation editor. The signal can block the under-frequency protection function.

Rate of change of frequency protection (81R)

The deviation of the frequency from the rated system frequency indicates unbalance between the generated power and the load demand. If the available generation is large compared to the consumption by the load connected to the power system, then the system frequency is above the rated value, and if it is small, the frequency is below the rated value. If the unbalance is large, then the frequency changes rapidly. The rate of change of frequency protection function is usually applied to reset the balance between generation and consumption to control the system frequency. Another possible application is the detection of unintended island operation of distributed generation and some consumers. In the island, there is low probability that the power generated is the same as consumption; accordingly, the detection of a high rate of change of frequency can be an indication of island operation.

The rate of change of frequency protection function generates a start signal if the df/dt value is above the setting value. The rate of change of frequency is calculated as the difference of the frequency at the present sampling and at three periods earlier. Time delay can also be set.



The function can be enabled/disabled by a parameter.

The rate of change of frequency protection function has a binary input signal. The conditions of the input signal are defined by the user, applying the graphic equation editor. The signal can block the rate of change of frequency protection function.

Lockout trip logic (86/94)

The lockout version of the simplified trip logic function operates according to the functionality required by the IEC 61850 standard for the "Trip logic logical node". Its output can be set to lockout and be reset externally.

This simplified software module can be applied if only threephase trip commands are required, that is, phase selectivity is not applied.

The function receives the trip requirements of the protective functions implemented in the device and combines the binary signals and parameters to the outputs of the device.

The operation can be normal or lockout. In normal mode, the output remains energized at least for a given pulse time and drops off as soon as the trip input drops off. The aim of this decision logic is to define a minimal impulse duration even if the protection functions detect a very shorttime fault.

In lockout mode the output stays active until the function gets a reset signal on its reset input.

The trip requirements and the reset signal are programmed by the user, using the graphic equation editor.

Restricted earth fault protection (87N)

The restricted earth-fault protection function is basically a low-impedance differential protection function based on sequence current zero components. It can be applied to protect one side winding of transformers with grounded neutral against single-phase-to-



earth fault (see Figure). The function compares the measured neutral current at the star point (IN) and the calculated zero sequence current component of the phase currents (IL1, IL2, IL3) and generates a trip command if the difference of these currents is above the characteristics.

The function performs the necessary calculations for the evaluation of the "percentage differential characteristics", and



decides to trip if the differential current is above the characteristic curve of the zero sequence differential protection function. This curve is the function of the restraint (Bias) current, which is the maximum of the phase currents and the current of the neutral point.

Additionally, the function compares the direction of the neutral current and that of the calculated zero sequence current. In case of small zero sequence component of the high fault currents in the phases, this decision improves the stability of the function.

A Boolean parameter of the restricted earth-fault protection function serves to enable the directional checking of the measured and calculated zero sequence currents. The restricted earth-fault protection function generates a trip signal if the differential current as the function of the bias current is above the differential characteristic lines and the function is not blocked by the directional decision. Additionally, the operation of the function is enabled by parameter setting. The conditions of enabling are defined by the user applying the graphic equation editor.

Switch-onto-fault (SOFT)

Some protection functions, e.g. distance protection, directional overcurrent protection, etc. need to decide the direction of the fault. This decision is based on the angle between the voltage and the current. In case of close-up faults, however, the voltage of the faulty loop is near zero: it is not sufficient for a directional decision. If there are no healthy phases, then the voltage samples stored in the memory are applied to decide if the fault is forward or reverse.

If the protected object is energized, the close command for the circuit breaker is received in "dead" condition. This means that the voltage samples stored in the memory have zero values. In this case the decision on the trip command is based

on the programming of the protection function for the "switchonto-fault" condition.

This "switch-onto-fault" detection function prepares the conditions for the subsequent decision. The function can handle both automatic and manual close commands.

MEASUREMENT FUNCTIONS

Measurement functions

Based on the hardware inputs the measurements listed in Table below are available.

| Measurement functions | Var. 0 | Var. 1 | Var. 2 | Var. 6 |
|--|--------|--------|--------|--------|
| Current (I1, I2, I3, I4, Iseq (I0, I1, I2)) | | X | X | |
| Voltage (U1, U2, U3, U4, U12, U23, U31, Useq (U0, U1, U2)) and frequency | | | X | X |
| Power (P, Q, S, pf) and Energy (E+, E-, Eq+, Eq-) | | | X | |
| Circuit breaker wear | X | X | X | |
| Supervised trip contacts (TCS) | X | X | X | |

The measurement functions of the IED Ep+ S24 configuration

Monitoring functions

The IED EP+ S24 Series product type can monitor and detect current and voltage harmonics and short duration system disturbances such as:

- Harmonics contents of each voltage and current channel (order 1st to order 19th)
- Current total demand distortion (TDD)
- Voltage total harmonic distortion (THD)
- Sags (Dips), Swells and Interrupts



HMI & COMUNICATION

HMI AND COMMUNICATION TASKS

- Embedded WEB-server: Allows remote access via Ethernet port of device
 - Firmware upgrade possibility •
 - Modification of user parameters •
 - Events list and disturbance record .
 - Password management
 - Online data measurement
 - Commands

.

0

0

0

0

0

Administrative tasks

Front panel HMI:

HMI+2504:

There are two type of front panel HMI:

 128x64pixels, black and white 4 x tactile user keys

RJ45 10/100Mbit/s

HMI+2404 (optional):

4 x tactile user keys

RJ-45 10/100Mbit/s



Embedded WEB-server

EUROPROT -On-line data Line measureme Active Power -0.00 kW

IED EP+S24 B&W HMI front panel



IED EP+S24 True color HMI front panel

Communication:

- The built-in 5-port Ethernet switch allows EuroProt+ to connect to IP/Ethernet-based network. The following Ethernet ports are • available:
 - Station bus (100Base-FX Ethernet) SBW
 - Redundant station bus (100Base-FX Ethernet) SBR 0

320 × 240 pixel TFT display with Resistive touchscreen interface

- Proprietary process bus (100Base-FX Ethernet) 0
- RJ-45 Ethernet user interface 0
- Optional 10/100Base-T port via RJ-45 connector
- PRP/HSR seamless redundancy for Ethernet networking (100Base-FX Ethernet; 10/100Base-TX Ethernet)
- Redundancy RJ-45 for Ethernet networking (10/100Base-TX Ethernet)
- Other communication:
 - RS422/RS485 interfaces (galvanic interface to support legacy or other serial protocols, ASIF)
 - Plastic or glass fiber interfaces to support legacy protocols, ASIF 0

FUNCTIONAL PARAMETERS

000

| Circuit breaker control function block (CB1 | Pol) |
|---|--|
| ControlModel Forced check | Direct normal, Direct enhanced, SBO enhanced If true, then the check function cannot be neglected by the check attribute defined by the IEC 61850 standard |
| Max.Operating time | 10-1000ms in 1ms steps |
| | |
| Pulse length | 50-500ms in 1ms steps |
| Max.Intermediate time | 20-30000ms in 1ms steps |
| Max.SynChk time | 10-5000ms in 1ms steps |
| Max.SynSW time | 0-60000ms in 1ms steps |
| SBO Timeout | 1000-20000ms in 1ms steps |
| Disconnector control function (DisConn) | |
| ControlModel | Direct normal, Direct enhanced, SBO enhanced |
| Type of switch | N/A, Load break, Disconnector, Earthing Switch, HS |
| | Earthing Switch |
| Forced check | If true, then the check function cannot be neglected by the |
| | check attribute defined by the IEC 61850 standard |
| Max.Operating time | 10-20000ms in 1ms steps |
| Pulse length | 50-30000ms in 1ms steps |
| Max.Intermediate time | 20-30000ms in 1ms steps |
| SBO Timeout | 1000-20000ms in 1ms steps |
| Overexcitation protection function (24) | |
| Operation | Off, Definite Time, IEEE |
| Start U/f LowSet | 80-140% in 1% steps |
| Start U/f HighSet | 80-140% in 1% steps |
| Time Multiplier | 1-100 in 1 steps |
| Min Time Delay | 0.50-60.00s in 0.01s steps |
| Max Time Delay | 300.00-8000.00s in 0.01s step |
| Cooling Time | 60.00-8000.00s in 0.01s step |
| Synchro check (25) | |
| Voltage Select | L1-N, L2-N, L3-N, L1-L2, L2-L3, L3-L1 |
| Voltage Select | Off, On, ByPass |
| SynSW Auto | Off, On |
| Energizing Auto | Off, DeadBus LiveLine, LiveBus DeadLine, Any energ case |
| Operation Man | Off, On, ByPass |
| SynSW Man | Off, On |
| Energizing Man | Off, DeadBus LiveLine, LiveBus DeadLine, Any energ case |
| U Live | 60-110% in 1% steps |
| U Dead | 10-60% in 1% steps |
| | |
| Udiff Syncheck auto Udiff SynSW auto | 5-30% in 1% steps |
| MaxPhaseDiff auto | 5-30% in 1% steps |
| Udiff SynCheck Man | 5-80° in 1° steps |
| | 5-30% in 1% steps |
| Udiff SynSW Man | 5-30% in 1% steps |
| MaxPhaseDiff Man | 5-80° in 1° steps |
| FrDiff SynCheck Auto | 0.02-0.5Hz in 0.02Hz steps |
| FrDiff SynSW Auto | 0.10-1.00Hz in 0.2Hz steps |
| FrDiff SynCheck Man | 0.02-0.5Hz in 0.02Hz steps |

 \square





FUNCTIONAL PARAMETERS

| FrDiff SynSW Man | 0.10.1.00Hz in 0.2Hz stops |
|---|---|
| Breaker Time | 0.10-1.00Hz in 0.2Hz steps 0-500ms in 1ms steps |
| Close Pulse | 10-60000ms in 1ms steps |
| Max Switch Time | 100-60000ms in 1ms steps |
| | |
| Definite time undervoltage protection (27) | |
| Operation | Off, 1 out of 3, 2 out of 3, All |
| Start Voltage | 30-130% in 1% steps |
| Block Voltage | 0-20% in 1% steps |
| Reset Ratio | 1-10% in 1% steps |
| Time Delay | 50-60000ms in 1ms steps |
| Directional overpower protection (32) | |
| Operation | Off, On |
| Direction Angle | -179-180deg in 1deg steps |
| Start Power | 1-200% in 0.1% steps |
| Time Delay | 0-60000ms in 1ms steps |
| Directional underpower protection (37) | |
| Operation | Off, On |
| Direction Angle | -179-180deg in 1deg steps |
| Start Power | 1-200% in 0.1% steps |
| Time Delay | 0-60000ms in 1ms steps |
| - | |
| Loss-of-load (undercurrent) (37) | |
| Operation | Off, On |
| Operation | OII, OII |
| Start signal only | False, True |
| - | |
| Start signal only | False, True |
| Start signal only Start Current | False, True 20-100% in 1% steps |
| Start signal only Start Current Idle Current | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) |
| Start signal only Start Current Idle Current Time delay | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection Operation | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection Operation Start Current Time Multiplier | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI VeryInv, S-3000% in 1% steps 0.05-999 in 0.01 steps |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection Operation Start Current Time Multiplier Minimal time delay for the inverse char. | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI VeryInv, ANSI ExtInv, 5-3000% in 1% steps 0.05-999 in 0.01 steps 0-60000ms in 1ms steps |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection Operation Start Current Time Multiplier | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI VeryInv, S-3000% in 1% steps 0.05-999 in 0.01 steps |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection Operation Start Current Time Multiplier Minimal time delay for the inverse char. Definite time delay | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI VeryInv, ANSI ExtInv, 5-3000% in 1% steps 0.05-999 in 0.01 steps 0-60000ms in 1ms steps 0-60000ms in 1ms steps |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection Operation Start Current Time Multiplier Minimal time delay for the inverse char. Definite time delay Reset time delay for the inverse char Broken conductor protection (46BC) | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 0.05-999 in 0.01 steps 0-60000ms in 1ms steps 0-60000ms in 1ms steps 0-60000ms in 1ms steps |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection Operation Start Current Time Multiplier Minimal time delay for the inverse char. Definite time delay Reset time delay for the inverse char Broken conductor protection (46BC) Operation | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 0.05-999 in 0.01 steps 0-60000ms in 1ms steps 0-60000ms in 1ms steps 0-60000ms in 1ms steps |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection Operation Start Current Time Multiplier Minimal time delay for the inverse char. Definite time delay Reset time delay for the inverse char Broken conductor protection (46BC) | False, True20-100% in 1% steps1-20% in 1% steps0-60000ms in 1ms steps(46)Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IECLongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 0.05-999 in 0.01 steps 0-60000ms in 1ms steps 0-60000ms in 1ms steps 0-60000ms in 1ms stepsOff, On False, True |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection Operation Start Current Time Multiplier Minimal time delay for the inverse char. Definite time delay Reset time delay for the inverse char Broken conductor protection (46BC) Operation Start signal only | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 0.05-999 in 0.01 steps 0-60000ms in 1ms steps 0-60000ms in 1ms steps 0-60000ms in 1ms steps |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection Operation Start Current Time Multiplier Minimal time delay for the inverse char. Definite time delay Reset time delay for the inverse char Broken conductor protection (46BC) Operation Start signal only Start current | False, True20-100% in 1% steps1-20% in 1% steps0-60000ms in 1ms steps(46)Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IECLongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps0.05-999 in 0.01 steps0-60000ms in 1ms steps0.05.999 in 0.01 steps0-60000ms in 1ms steps0-60000ms in 1ms steps0.60000ms in 1ms steps0.60000ms in 1ms steps0.01 steps0.05.999 in 10.01 steps0.05.999 in 0.01 steps0.05.999 in 0.01 steps0.060000ms in 1ms steps0.060000ms in 1ms steps0.060000ms in 1ms steps10.90% in 1% steps10.90% in 1% steps100-60000ms in 1ms steps |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection Operation Start Current Time Multiplier Minimal time delay for the inverse char. Definite time delay Reset time delay for the inverse char Broken conductor protection (46BC) Operation Start signal only Start current Time Delay Negative sequence overvoltage protection | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongIvr, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 0.60000ms in 1ms steps 0.60000ms in 1ms steps 0-60000ms in 1ms steps 10-90% in 1% steps 100-60000ms in 1ms steps 100-60000ms in 1ms steps |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection Operation Start Current Time Multiplier Minimal time delay for the inverse char. Definite time delay Reset time delay for the inverse char Broken conductor protection (46BC) Operation Start signal only Start current Time Delay Negative sequence overvoltage protection Operation | False, True20-100% in 1% steps1-20% in 1% steps0-60000ms in 1ms steps(46)Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IECLongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps0.05-999 in 0.01 steps0-60000ms in 1ms steps0.60000ms in 1ms steps00ff, OnFalse, True10-90% in 1% steps100-60000ms in 1ms steps00ff, OnFalse, True10-90% in 1% steps00ff, OnFalse, True00-60000ms in 1ms steps00ff, On |
| Start signal only Start Current Idle Current Time delay Negative sequence overcurrent protection Operation Start Current Time Multiplier Minimal time delay for the inverse char. Definite time delay Reset time delay for the inverse char Broken conductor protection (46BC) Operation Start signal only Start current Time Delay Negative sequence overvoltage protection | False, True 20-100% in 1% steps 1-20% in 1% steps 0-60000ms in 1ms steps (46) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongIv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 0.60000ms in 1ms steps 0.60000ms in 1ms steps 0-60000ms in 1ms steps 10-90% in 1% steps 100-60000ms in 1ms steps 100-60000ms in 1ms steps |



| Motor startup supervision (48/66) | |
|---|--|
| Operation | Off, On |
| InMotor/InCT | 20-150% in 1% steps |
| Start-up Current | 50-1000% in 1% steps |
| Idle Current | 5-50% in 1% steps |
| Start-up Time | 1-100s in 1s steps |
| Restart Time | 10-5000s in 1s steps |
| No. of Startup | 0-5 in 1 steps |
| Thermal protection (49) | |
| Operation | Off, Pulsed, Locked |
| Alarm Temperature | 60-200deg in 1deg steps |
| Trip Temperature | 60-200deg in 1deg steps |
| Rated Temperature | 60-200deg in 1deg steps |
| Base Temperature | 0-40deg in 1deg steps |
| Unlock Temperature | 20-200deg in 1deg steps |
| Ambient Temperature | 0-40deg in 1deg steps |
| Startup Term | 0-60% in 1% steps |
| Rated Load Current | 20-150% in 1% steps |
| Time Constan | 1-999min in 1min step |
| Three-phase instantaneous overcurrent pi | rotection (50) |
| | |
| Operation | Off, Peak value, Fundamental value |
| Start current | 5-3000% in 1% steps |
| Breaker failure protection (50BF) | |
| Operation | Off, Current, Contact, Current/Contact |
| Retrip | Off, On |
| Start Ph Current | 20-200% in 1% steps |
| Start Res Current | 10-200% in 1% steps |
| Retrip Time Delay | 0-1000ms in 1ms steps |
| Backup Time Delay | 60-1000ms in 1ms steps |
| Pulse Duration | 0-60000ms in 1ms steps |
| Residual instantaneous overcurrent prote | ction (50N/50Ns) |
| Operation | Off, Peak value, Fundamental value |
| Start Current | 5-3000% in 1% steps |
| Three-phase time overcurrent protection (| 51) |
| | |
| Operation | Off, Definite Time, IEC Inv, IEC VeryInv, IEC ExtInv, IEC LongInv, ANSI0.95 Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv |
| Start current | 5-3000% in 1% steps |
| Time Multiplier | 0.05-999 in 0.01 steps |
| Minimum time delay for the inverse char. | 40-60000ms in 1ms steps |
| Definite time delay for definite type char. | 40-60000ms in 1ms steps |
| Reset time delay for the IEC type inverse | 60-60000ms in 1ms steps |
| char. | |
| Capacitor unbalance protection (54C) | |
| Capacitor unbalance protection (51C) | |

۵.,

'*



| | LongInv, ANSI 0.95 Inv, ANSI Modinv, ANSI VeryInv, ANSI Start Current 1 10100% in 1% steps Start Current 2 5-100% in 1% steps Inme Multiplier 0.05-999 in 0.01 steps Definite time delay 0-60000ms in 1ms steps Reset time 0-60000ms in 1ms steps Operation Off, Definite Time, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Modinv, ANSI VeryInv, ANSI ExtInv Start current In 1 A or 5A In = 200mA or 1A 5-3000% in 1% steps Time Multiplier 0.05-999 in 0.01 steps Minimum time delay to react the inverse char. 5-3000% in 1% steps Start current In 1 A or 5A In = 200mA or 1A 5-3000% in 1% steps Start current ANSI LongInv, ANSI LongExtInv In = Multiplier 0.05-999 in 0.01 steps Monimum time delay for the inverse char. 40-60000ms in 1ms steps Voltage dependent overcurrent protection (51V) Controlled Start Current 20-300% in 1% steps U jowlimit 20-60% in 1% steps U jowlimit 20-60% in 1% steps U jowlimit 20-60% in 1% steps Start Vo | | 1 |
|---|---|---|--|
| Start Current 1 10-100% in 1% steps Start Current 2 5-100% in 1% steps Definite Time Multiplier 0.05-999 in 0.01 steps Minimum time delay 0-60000ms in 1ms steps Definite Time delay 0-60000ms in 1ms steps Reset time 0-60000ms in 1ms steps Operation Off, DefiniteTime, IEC Inv,IEC Verytinv, IEC ExtInv,IEC In = 1A or 5A 5-3000% in 1% steps In = 1A or 5A 5-3000% in 1% steps In = 200m A or 1A 5-3000% in 1% steps Time Multiplier 0.05-999 in 0.01 steps Minimum time delay for the inverse char. 40-60000ms in 1ms steps Definite time delay for the inverse char. 60-60000ms in 1ms steps Definite time delay for the inverse char. 60-60000ms in 1ms steps Voltage dependent overcurrent protection (51V) 50000ms in 1ms steps Operation Off, On Restr. Mode Restrained, Controlled Start Current 20-600% in 1% steps U.lowlimit 20-60% in 1% steps Start Voltage 9-60000ms in 1ms steps | Start Current 1 10-100% in 1% steps Start Current 2 5-100% in 1% steps Inom Capacitor 15-120% in 1% steps Time Multiplier 0.05-999 in 0.01 steps Definite time delay 0-60000ms in 1ms steps Definite time delay 0-60000ms in 1ms steps Definite time delay 0-60000ms in 1ms steps Deparation Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv Start current In = 1 A or 5A In = 20 mA or 1A 5-3000% in 1% steps Time Multiplier 0.05-999 in 0.01 steps Minimum time delay for the inverse char. 0-60000ms in 1ms steps Definite time delay for the inverse char. 0-60000ms in 1ms steps Ottage dependent overcurrent protection (51V) 00 Operation Off, On Restration Off, On Restration 0-60000ms in 1% steps U-julpilimit 20-60% in 1% steps Define time delay 0ff, On Start Voltage 30-130% in 1% | Operation | LongInv, ANSI0.95 Inv, ANSI ModInv, ANSI VeryInv, ANSI |
| Start Current 25-100% in 1% stepsInom Capacitor15-120% in 1% stepsMinimum time delay0-60000ms in 1ms stepsDefinite time delay0-60000ms in 1ms stepsReset time0-60000ms in 1ms stepsReset time0-60000ms in 1ms stepsOperationChefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInvStart currentIn = 1A or 5A n = 200mA or 1AIn = 1A or 5A n = 200mA or 1A5-3000% in 1% stepsOperationOff. On Chefon00ms in 1ms stepsVoltage dependent overcurrent protection (51N-V-VDefinite Time delay for the inverse char.40-60000ms in 1ms stepsOperationOff. On Restr. ModeReset time delay for the inverse char.40-60000ms in 1ms stepsVoltage dependent overcurrent protector+VOperationOff. On Restr. ModeRestr. ModeRestrained, Controlled 20-3000% in 1% stepsL_invilinit20-60% in 1% stepsL_invilinit20-60% in 1% stepsL_invilinit20-60% in 1% stepsTime delayOff. On 30-130% in 1% stepsStart OursetSolotoms in 1ms stepsDefinite time overvoltage protection (fSU)OperationOff. On Start VoltageReset RatioOff. On 11 % stepsTime DelayOff. On 14% stepsCapacitor overvoltage protection (fSU)OperationOff. On 15-120% in 1% stepsReset Ratio15-120% in 1% stepsTime DelayOff. On 15-120% in 1% steps <td>Start Current 2 5-100% in 1% steps Inom Capacitor 15-120% in 1% steps Time Multiplier 0.65.999 in 0.01 steps Minimum time delay 0-60000ms in 1ms steps Definite time delay 0-60000ms in 1ms steps Reset time 0-60000ms in 1ms steps Operation Off. DefiniteTime, IEC Inv.IEC VeryInv, IEC Extinv.IEC Longinv, ANSI Ixv, ANSI Modinv, ANSI VeryInv, ANSI Extinv ANSI Longinv, ANSI LongVeryInv, ANSI LongExtInv Start current 5-3000% in 1% steps In = 1A or 5A 5-3000% in 1% steps Start current 6-60000ms in 1ms steps Minimum time delay for the inverse char. 40-60000ms in 1ms steps Oberation Off. On Reset time delay for the inverse char. 60-60000ms in 1ms steps Voltage dependent overcurrent protection (51V) Operation Off. On Rest: Mode Restrained, Controlled Start Current 20-60% in 1% steps U_lowlimit 20-60% in 1% steps</td> <td>Start Current 4</td> <td></td> | Start Current 2 5-100% in 1% steps Inom Capacitor 15-120% in 1% steps Time Multiplier 0.65.999 in 0.01 steps Minimum time delay 0-60000ms in 1ms steps Definite time delay 0-60000ms in 1ms steps Reset time 0-60000ms in 1ms steps Operation Off. DefiniteTime, IEC Inv.IEC VeryInv, IEC Extinv.IEC Longinv, ANSI Ixv, ANSI Modinv, ANSI VeryInv, ANSI Extinv ANSI Longinv, ANSI LongVeryInv, ANSI LongExtInv Start current 5-3000% in 1% steps In = 1A or 5A 5-3000% in 1% steps Start current 6-60000ms in 1ms steps Minimum time delay for the inverse char. 40-60000ms in 1ms steps Oberation Off. On Reset time delay for the inverse char. 60-60000ms in 1ms steps Voltage dependent overcurrent protection (51V) Operation Off. On Rest: Mode Restrained, Controlled Start Current 20-60% in 1% steps U_lowlimit 20-60% in 1% steps | Start Current 4 | |
| Inom Capacitor15-120% in 1% stepsTime Multiplier0.05-999 in 0.01 stepsMinimu time delay0-60000ms in 1ms stepsPeriline time delay0-60000ms in 1ms stepsReset time0-60000ms in 1ms stepsReset time0-60000ms in 1ms stepsPerationOff, Definite Time, IEC Inv.IEC VeryInv, IEC ExtInv.IECLonginv, ANSI Longinv, ANSI Longivr, ANSI Longivr, ANSI LongistrinvStart currentIn = 1A or 5A5-3000% in 1% stepsIn = 200mA or 1A5-3000% in 1% stepsTime Multiplier0.05-999 in 0.01 stepsMinimu time delay for the inverse char.40-60000ms in 1ms stepsPefinite time delay for the inverse char.40-60000ms in 1ms stepsPofinite time delay for the inverse char.40-60000ms in 1ms stepsVoltage dependent overcurrent protection (511)OperationOff, OnRestr. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU.lowlimit20-60% in 1% stepsU.lowlimit20-60% in 1% stepsU.lowlimit20-60% in 1% stepsU.lowlimit20-60% in 1% stepsStart Voltage0ff, OnReset Ratio30-130% in 1% stepsTime delay0-60000ms in 1ms stepsPerilet ime overvoltage protection functionVoltage0ff, OnRated Current0ff, OnRated Current0ff, OnRated Current0ff, OnRated Current0ff, OnRated Current0ff, OnRated Current0ff, OnRated | Inom Capacitor15-120% in 1% stepsTime Multiplier0.05-999 in 0.01 stepsDefinite time delay0-60000ms in 1ms stepsReset time0-60000ms in 1ms stepsReset time0-60000ms in 1ms stepsCoparationOff, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv,ANSI ModInv, ANSI VeryInv, ANSI ExtInv ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInvStart current5-3000% in 1% stepsIn = 1A or 5A In = 200mÅ or 1A5-3000% in 1% stepsStart current0.05-999 in 0.01 stepsMinimum time delay for the inverse char.40-60000ms in 1ms stepsPolitike time delay for the inverse char.40-60000ms in 1ms stepsVoltage dependent overcurrent protection (51V)OperationOff, On Restr. ModeRest: ModeRestrained, Controlled Start CurrentJoynHintit20-60% in 1% stepsU highlimit60-110% in 1% stepsU highlimit60-110% in 1% stepsDerationOff, On Start VoltageReset Ratio1-10% in 1% stepsDerationOff, On Start VoltageReset Ratio0.01 N 1-10% in 1% stepsCharter time overvoltage protection functionCVOperationOff, On 1-10% in 1% stepsReset Ratio1-10% in 1% stepsCharter time delay0-60000ms in 1ms stepsDefinite time overvoltage protection functionCVOperationOff, On 1-10% in 1% stepsReset Ratio1-10% in 1% stepsCapacitor overvoltage protection function0-10000ms in 1ms ste | | • |
| Time Multiplier0.05-999 in 0.01 stepsMinimum time delay0-60000ms in 1ms stepsReset time0-60000ms in 1ms stepsReset time0-60000ms in 1ms stepsOperationOff, Definite Time, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI Modinv, ANSI VeryInv, ANSI ExtInv ANSI LongInv, ANSI Inv, ANSI Modinv, ANSI VeryInv, ANSI ExtInv ANSI LongInv, ANSI Inv, ANSI LongVeryInv, ANSI LongExtInvStart current5-3000% in 1% stepsIn = 1A or 5A In = 200mA or 1A5-3000% in 1% stepsTime Multiplier0.05-999 in 0.01 stepsMinimum time delay for the inverse char.6-60000ms in 1ms stepsDefinite time delay for definite type char.40-60000ms in 1ms stepsReset time delay for the inverse char.6-60000ms in 1ms stepsVoltage dependent overcurrent protection (SUV)OperationOff, On Restr. ModeRestr. ModeRestrained, Controlled Start CurrentU_lowlimit20-60% in 1% stepsU_lowlimit20-60% in 1% stepsU_lowlimit20-60% in 1% stepsU_lowlimit20-60% in 1% stepsTime delayOff, On Start VoltageReset Ratio1-10% in 1% stepsTime Delay0ff, On 15-120% in 1% stepsOperationOff, On 15-120% in 1% stepsMaring Start80-120% in 1% stepsWarning Start90-2000 sin 1ms stepsWarning Start15-120% in 1% stepsWarning Delay15-6000ms in 1ms stepsWarning Delay15-120% in 1% stepsWarning Delay15-20% in 1% steps< | Time Multiplier0.05-999 in 0.01 stepsMinimum time delay0-60000ms in 1ms stepsReset time0-60000ms in 1ms stepsReset time0-60000ms in 1ms stepsOperationOff, DefiniteTime, IEC Inv.IEC VeryInv, IEC ExtInv.IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv ANSI LongIvv, ANSI LongVeryInv, ANSI LongExtInvStart currentb-3000% in 1% stepsIn = 1A or 5A In = 200mA or 1A5-3000% in 1% stepsStart currentb-3000% in 1% stepsMinimum time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for the inverse char.60-60000ms in 1ms stepsReset time delay for the inverse char.60-60000ms in 1ms stepsVoltage dependent overcurrent protection5-1000% in 1% stepsVoltage dependent overcurrent protection5-1000% in 1% stepsU_highlimit20-60% in 1% stepsU_highlimit20-60% in 1% stepsU_highlimit20-60% in 1% stepsU_highlimit20-60% in 1% stepsDefinite time overvoltage protection (59)OperationOff, On start VoltageReset Ratio1-10% in 1% stepsStart Current90-10000ms in 1ms stepsDefinite time overvoltage protection functionVoltage30-130% in 1% stepsLing Ling Ling Ling Ling Ling Ling Ling | | |
| Minimum time delay 0-60000ms in 1ms steps Definite time delay 0-60000ms in 1ms steps Reset time 0-60000ms in 1ms steps Residual time overcurrent protection (51\//5\/\steps) Operation Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI Long VeryInv, ANSI ExtInv ANSI LongInv, ANSI Long VeryInv, ANSI Long ExtInv Start current In = 10 or 5A In = 200mA or 1A 5-3000% in 1% steps Time Multiplier 005-999 in 0.01 steps Minimum time delay for the inverse char. 40-60000ms in 1ms steps Definite time delay for the inverse char. 60-60000ms in 1ms steps Voltage dependent overcurrent protection (51\/ Operation Off, On Restr. Mode Restrained, Controlled Start Current 20-60000ms in 1ms steps U_lowlimit 20-60000ms in 1% steps U_lowlimit 20-60000ms in 1ms steps U_lowlimit 20-60000ms in 1ms steps Definite time overvoltage protection (59) Operation Off, On Start Voltage 30-130% in 1% steps U_lowlimit 0-60000ms in 1ms steps Definite time overvoltage protection function 50000ms in 1ms steps Definite time overvoltage protection function 50000ms in 1ms steps Definite time overvoltage protection function <td>Minimum time delay 0-60000ms in 1ms steps Definite time delay 0-60000ms in 1ms steps Reset time 0-60000ms in 1ms steps Restual time overcurrent protection (\$1N/>TINS) Operation Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI I.v., ANSI ModInv, ANSI VeryInv, ANSI ExtInv Start current In = 1A or 5A In = 200mA or 1A 5-3000% in 1% steps Definite time delay for the inverse char. 40-60000ms in 1ms steps Voltage dependent overcurrent protection (\$1// 40-60000ms in 1ms steps Voltage dependent overcurrent protection (\$1// 0.05 -999 in 0.01 steps Voltage dependent overcurrent protection (\$1// 00-60000ms in 1ms steps Voltage dependent overcurrent protection (\$1// 00-60000ms in 1ms steps Voltage dependent overcurrent protection (\$1// 00-60000ms in 1ms steps Voltage dependent overcurrent protection (\$1// 00-60000ms in 1ms steps U_lowlimit 20-60% in 1% steps U_lowlimit 0-60000ms in 1ms steps Deperation Off, On Start Voltage 0-60000ms in 1ms steps <!--</td--><td>-</td><td></td></td> | Minimum time delay 0-60000ms in 1ms steps Definite time delay 0-60000ms in 1ms steps Reset time 0-60000ms in 1ms steps Restual time overcurrent protection (\$1N/>TINS) Operation Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI I.v., ANSI ModInv, ANSI VeryInv, ANSI ExtInv Start current In = 1A or 5A In = 200mA or 1A 5-3000% in 1% steps Definite time delay for the inverse char. 40-60000ms in 1ms steps Voltage dependent overcurrent protection (\$1// 40-60000ms in 1ms steps Voltage dependent overcurrent protection (\$1// 0.05 -999 in 0.01 steps Voltage dependent overcurrent protection (\$1// 00-60000ms in 1ms steps Voltage dependent overcurrent protection (\$1// 00-60000ms in 1ms steps Voltage dependent overcurrent protection (\$1// 00-60000ms in 1ms steps Voltage dependent overcurrent protection (\$1// 00-60000ms in 1ms steps U_lowlimit 20-60% in 1% steps U_lowlimit 0-60000ms in 1ms steps Deperation Off, On Start Voltage 0-60000ms in 1ms steps </td <td>-</td> <td></td> | - | |
| Definite time delay Reset time0-60000ms in 1ms steps 0-60000ms in 1ms stepsResidual time overcurrent protection (S1N/S1Ns)OperationOff, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInvStart current In = 1A or 5A In = 200mA or 1A5-3000% in 1% steps 5-3000% in 1% stepsMinimum time delay for the inverse char. Definite time delay for the inverse char.5-3000% in 1% stepsOttage dependent overcurrent protection5-1000ms in 1ms stepsVoltage dependent overcurrent protection5-1000ms in 1ms stepsVoltage dependent overcurrent protection5-1000ms in 1% stepsU_lowlinit U_lowlinit20-60% in 1% stepsU_lowlinit U_lowlinit20-60% in 1% stepsU_lowlinit U_lowlinit20-60% in 1% stepsU_lowlinit U_lowlinit20-60% in 1% stepsDefinite time overvoltage protection forsion Time delayOff, On Start VoltageOperation Reset Ratio Time DelayOff, On 15-120% in 1% stepsCapacitor overvoltage protection function Varning Start Reset timeOff, On 15-120% in 1% stepsCapacitor overvoltage protection forsion Varning StartOff, On 15-120% in 1% stepsRest time Varning Delay0.52.000ms in 1ms stepsPeration Reset time16.0000ms in 1ms stepsQperation Time DelayOff, On 15-120% in 1% stepsCapacitor overvoltage protection forsion Varning Start Reset time0.52.0000ms in 1ms stepsQueration Reset time0.52.0000ms in 1ms s | Definite time delay 0-60000ms in 1ms steps Reset time 0-60000ms in 1ms steps Reset time 0-60000ms in 1ms steps Reset time 0-ff, DefiniteTime, IEC Inv,IEC VeryInv, IEC Extinv,IEC LongInv, ANSI LongIvv, ANSI LongVeryInv, ANSI VeryInv, ANSI Extinv ANSI LongIvr, ANSI LongVeryInv, ANSI LongExtInv Start current In = 1A or 5A In = 1A or 5A 5-3000% in 1% steps Imme Multiplier 0.5-399 in 0.01 steps Winimum time delay for the inverse char. 40-60000ms in 1ms steps Definite time delay for definite type char. 60-6000ms in 1ms steps Reset time delay for definite type char. 60-60000ms in 1ms steps Voltage dependent overcurrent protection (51V) 00 Operation Off, On Restr. Mode Restrained, Controlled Start Current 20-60% in 1% steps U_low/imit 20-60% in 1% steps Du/joilimit 20-60% in 1% steps Doperation Off, On Start Voltage 30-130% in 1% steps Definite time overvoltage protection function (59C) 00 Operation Off, On Start Voltage 1-10% in 1% steps Reset Ratio 1-10% in 1% st | - | · |
| Reset time 0-60000ms in 1ms steps Residual time overcurrent protection (51/V-1/V) Version Operation Of, Definite Time, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI Inv, ANSI Inv, ANSI LongExtInv Start current In = 1A or 5A In = 100mA or 1A 5-3000% in 1% steps Time Multiplier 0.05-999 in 0.01 steps Minimum time delay for the inverse char. 40-60000ms in 1ms steps Pefinite time delay for the inverse char. 60-60000ms in 1ms steps Reset time delay for the inverse char. 60-60000ms in 1ms steps Operation Off, On Restr. Mode Restrained, Controlled Start Current 20-60% in 1% steps U_lowlimit 20-60% in 1% steps Voltage 30-100% in 1% steps Time delay 90-60000ms in 1ms steps Definite time overvoltage protection (59) 10% in 1% steps Operation Off, On Start Voltage 30-130% in 1% steps Time Delay 0-60000ms in 1ms steps Operation Off, On Start Voltage 20-60% in 1% steps Time Delay 0-60000ms in 1ms steps <t< td=""><td>Reset time 0-60000ms in 1ms steps Residual time overcurrent protection (51N/51Ns) Operation Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI Modinv, ANSI VeryInv, ANSI ExtInv Start current In = 1A or 5A In = 200mA or 1A 5-3000% in 1% steps Time Multiplier 0.05-999 in 0.01 steps Minimum time delay for the inverse char. 0-60000ms in 1ms steps Definite time delay for definite type char. 80-60000ms in 1ms steps Reset time delay for the inverse char. 0-60000ms in 1ms steps Voltage dependent overcurrent protection 51V Operation Off, On Restr. Mode Restrained, Controlled Start Current 20-60% in 1% steps U low/imit 20-60% in 1% steps U_highlimit 60-110% in 1% steps U_highlimit 60-60000ms in 1ms steps U_highlimit 60-60000ms in 1ms steps U_highlimit 20-60% in 1% steps U_highlimit 60-60000ms in 1ms steps Deparation Off, On Start Current 20-60% in 1% steps U_highlimit 60-60000ms in 1ms steps Deparation Off, On Start Voltage 30-130% in 1% steps Operation Off, On Start Voltage</td><td>,</td><td></td></t<> | Reset time 0-60000ms in 1ms steps Residual time overcurrent protection (51N/51Ns) Operation Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI Modinv, ANSI VeryInv, ANSI ExtInv Start current In = 1A or 5A In = 200mA or 1A 5-3000% in 1% steps Time Multiplier 0.05-999 in 0.01 steps Minimum time delay for the inverse char. 0-60000ms in 1ms steps Definite time delay for definite type char. 80-60000ms in 1ms steps Reset time delay for the inverse char. 0-60000ms in 1ms steps Voltage dependent overcurrent protection 51V Operation Off, On Restr. Mode Restrained, Controlled Start Current 20-60% in 1% steps U low/imit 20-60% in 1% steps U_highlimit 60-110% in 1% steps U_highlimit 60-60000ms in 1ms steps U_highlimit 60-60000ms in 1ms steps U_highlimit 20-60% in 1% steps U_highlimit 60-60000ms in 1ms steps Deparation Off, On Start Current 20-60% in 1% steps U_highlimit 60-60000ms in 1ms steps Deparation Off, On Start Voltage 30-130% in 1% steps Operation Off, On Start Voltage | , | |
| Residual time overcurrent protection (51N/51N5) Operation Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv ANSI LongIv, ANSI LongVeryInv, ANSI LongExtInv Start current In = 1A or 5A 5-3000% in 1% steps In = 200mA or 1A 5-3000% in 1% steps Definite Time Multiplier 0.05-999 in 0.01 steps Minimum time delay for the inverse char. 40-60000ms in 1ms steps Definite Time delay for the inverse char. 60-60000ms in 1ms steps Voltage dependent overcurrent protection 5/1/ Operation Off, On Restr. Mode Restrained, Controlled Start Current 20-3000% in 1% steps U_lowlimit 20-60% in 1% steps U_lowlimit 20-60% in 1% steps U_lingHimit 60-110% in 1% steps Time delay 0 off, On Start Voltage 30-130% in 1% steps Definite time overvoltage protection (59) 0 Operation Off, On Start Voltage 0 off, On Start Voltage 0 off, On Start Voltage 0/100ms in 1% steps Time Delay 0 off, On Reset Rat | Rasidual time overcurrent protection (51N/51Ns) Operation Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI Inv, ANSI LongVeryInv, ANSI ExtInv ANSI LongVeryInv, ANSI LongExtInv Start current In = 1 A or 5A 5-3000% in 1% steps In = 200mA or 1A 5-3000% in 1% steps 0.05-999 in 0.01 steps Minimum time delay for the inverse char. 40-60000ms in 1ms steps 0.06-999 in 0.01 steps Operation Off, On 8estertime delay for definite type char. 40-60000ms in 1ms steps Operation Off, On Restrained, Controlled 20-3000% in 1% steps Voltage dependent overcurrent protection (51V) 0000ms in 1ms steps 0.6000ms in 1ms steps Operation Off, On Restrained, Controlled 20-60% in 1% steps U_lowlimit 20-60% in 1% steps 0.60000ms in 1ms steps Definite time overvoltage protection (59) 00000ms in 1ms steps 0.60000ms in 1ms steps Definite time overvoltage protection (59) 00000ms in 1ms steps 0.60000ms in 1ms steps Capacitor overvoltage protection function (59C) 0.60000ms in 1ms steps 0.60000ms in 1ms steps Operation Off, On 00000ms in 1ms steps 0.60000ms in 1ms steps 0.60000ms in 1ms steps | - | |
| Operation Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv Start current In = 1A or 5A In = 200mA or 1A Time Multiplier Operation Minimum time delay for the inverse char. Definite delay for the inverse char. Definite time delay for the inverse char. Operation Restr. Mode Start Current U_lowlinit 20-60% in 1% steps U_lowlinit 20-60% in 1% steps U_lowlinit 20-6000ms in 1ms steps Definite time overvoltage protection (\$9) Operation Start Voltage Start Voltage Reset Ratio 1-10% in 1% steps Time Delay Operation | Operation Off. DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC Start current In = 1A or 5A In = 200mA or 1A 5-3000% in 1% steps Time Multiplier 0.05-999 in 0.01 steps Minimum time delay for the inverse char. 60-60000ms in 1ms steps Definite time delay for the inverse char. 60-60000ms in 1ms steps Voltage dependent overcurrent protection (51V) 0ff. On Operation Off. On Reset Mode Restrained, Controlled Start Current 20-60% in 1% steps U_lowlimit 20-60% in 1% steps U_lowlimit 20-60% in 1% steps U_lowlimit 20-60% in 1% steps Definite time overvoltage protection (59) 0 Operation Off. On Start Voltage 30-130% in 1% steps Notage 30-130% in 1% steps Coff. On 30-130% in 1% steps Coff. On 30-130% in 1% steps Time Delay Off. On Start Voltage 30-130% in 1% steps Reset Ratio 1-10% in 1% steps Time Delay Off. On Start Voltage 30-120% in 1% steps | Residual time overcurrent protection (51) | V/51Ns) |
| LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInvStart currentIn = 1A or 5A In = 200mA or 1A5-3000% in 1% stepsTime Multiplier0.05-999 in 0.01 stepsMinimum time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for the inverse char.40-60000ms in 1ms stepsOperationCHTReset time delay for the inverse char.5-3000% in 1% stepsVoltage dependent overcurrent protection (51V)OperationCHT on Restr. ModeRestr. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU_lowlimit20-60% in 1% stepsU_lowlimit20-60% in 1% stepsU_lowlimit20-60% in 1% stepsKi_limit20-60% in 1% stepsTime delay0OperationOff, OnStart Voltage30-130% in 1% stepsTime Delay0OperationOff, OnStart Voltage30-130% in 1% stepsTime Delay0-60000ms in 1ms stepsOperationOff, OnStart Voltage30-130% in 1% stepsTime Delay0OperationOff, OnReset time15-120% in 1% stepsWarning Start80-120% in 1% stepsWarning Delay1-36000ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsParationOff, OnStart Voltage1-36000ms in 1ms stepsWarning Start80-120% in 1% stepsWarning Start1-3600ms in 1ms steps< | LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInvStart currentIn = 1A or 5AIn = 200mA or 1A5-3000% in 1% stepsTime Multiplier0.55-999 in 0.01 stepsMinimum time delay for the inverse char.Definite time delay for the inverse char.Definite time delay for the inverse char.OperationReset time delay for the inverse char.OperationRestr. ModeStart Current20-6000ms in 1ms stepsU_highlimit20-6000ms in 1% stepsDefinite time overvoltage protection (59)OperationOff, OnStart VoltageReset Ratio11 = 1/20% in 1% steps0-60000ms in 1ms stepsCapacitor overvoltage protection function(59)OperationReset time1-60000ms in 1ms stepsReset time1-60000ms in 1ms stepsReset time1-60000ms in 1ms stepsReset time1-60000ms in 1ms steps </td <td></td> <td></td> | | |
| ANSI Longluy, ANSI LongVeryInv, ANSI LongExtInvStart current In = 200mA or 1A5-3000% in 1% stepsTime Multiplier0.05-999 in 0.01 stepsMinimum time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for the inverse char.60-6000ms in 1ms stepsVoltage dependent overcurrent protectorVOperationOff, OnReset fime delayOff onRestr. ModeRestrained, ControlledStart Current20-600% in 1% stepsU_lowlimit20-60% in 1% stepsTime delay0OperationOff, OnStart Voltage30-130% in 1% stepsReset time1-10% in 1% stepsTime Delay0OperationOff, OnRated Current15-120% in 1% stepsYaming Start80-120% in 1% stepsYaming Delay1-3600ms in 1ms stepsYaming Dela | ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInvStart currentIn = 1A or 5AIn = 1A or 5A5-3000% in 1% stepsIme Multiplier0.05-999 in 0.01 stepsMinimum time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for the inverse char.60-60000ms in 1ms stepsReset time delay for the inverse char.60-60000ms in 1ms stepsOperationOff, OnRestr. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU_lowlimit20-60% in 1% stepsDefinite time overvoltage protection (59)OperationOff, OnStart Voltage0-ff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsWarning Delay1-3600ms in 1ms stepsWarning Delay0-ff, OnStart Voltage0-ff, OnStart Voltage0-ff, OnDerationOff, OnStart Voltage | oporation | |
| Start currentIn = 1A or 5A is = 200mA or 1A5-3000% in 1% steps 5-3000% in 1% stepsMinimum time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for the inverse char.60-60000ms in 1ms stepsVoltage dependent overcurrent protection (51V)OperationOff, On Restr. ModeRestr. ModeRestrained, ControlledStart Current20-300% in 1% stepsU_lowlimit20-60% in 1% stepsU_lowlimit20-60% in 1% stepsU_linglimit60-110% in 1% stepsIk_limit20-60% in 1% stepsTime delay0-60000ms in 1ms stepsDefrate time overvoltage protection (59)OperationOff, On 30-130% in 1% stepsStart Voltage30-130% in 1% stepsTime Delay0-60000ms in 1ms stepsCapacitor overvoltage protection function (59C)OperationOff, On 15-120% in 1% stepsReset fine160000ms in 1ms stepsVarning Start80-120% in 1% stepsWarning Delay1-3600ms in 1ms stepsYaming Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsReset time160000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsCapacital overvoltage protection (59L)Operation0 | Start current In = 1A or 5A 5-3000% in 1% steps In = 200mA or 1A 5-3000% in 1% steps Time Multiplier 40-60000ms in 1ms steps Minimum time delay for the inverse char. 40-60000ms in 1ms steps Definite time delay for definite type char. 60-60000ms in 1ms steps Reset time delay for the inverse char. 40-60000ms in 1ms steps Voltage dependent overcurrent protection (51V) 0ff, On Reset time delay for the inverse char. 0ff, On Rest. Mode Restrained, Controlled Start Current 20-600% in 1% steps U_lowlimit 20-60% in 1% steps U_lowlimit 20-60% in 1% steps U_lowlimit 20-60% in 1% steps Definite time overvoltage protection (59) 0ff, On Operation Off, On Start Voltage 30-130% in 1% steps Time Delay 0ff, On Start Outge protection function (59C) 0peration Operation Off, On Reset Ratio 15-120% in 1% steps 110% in 1% steps -60000ms in 1ms steps Capacitor overvoltage protection function (59C) 0ff, On Reset time 1 | | |
| In = 200mA or 1A5-3000% in 1% stepsTime Multiplier0.05-999 in 0.01 stepsMinimum time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for the inverse char.40-60000ms in 1ms stepsReset time delay for the inverse char.60-6000ms in 1ms stepsVoltage dependent overcurrent protection VVVOperationOff, OnRestr. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU_lowlimit60-110% in 1% stepsU_lwilmit60-110% in 1% stepsL_lighlimit60-110% in 1% stepsIk_limit20-60% in 1% stepsTime delay0OperationOff, OnStart Voltage30-130% in 1% stepsIk_limit20-6000ms in 1ms stepsDefinite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsTime DelayOff, OnStart Voltage protection functionCapacitor overvoltage protection function5-120% in 1% stepsNaming Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3800ms in 1ms stepsWarning Delay1-3800ms in 1ms stepsVarning Delay0-100 stepsOperation0ff, OnStart Voltage0-2-2 in 0.01 stepsWarning Delay1-3800ms in 1ms stepsOperation0ff, OnStart Voltage0-100 stepsWarning Delay1-3800ms in 1ms stepsOperation0ff, On | In = 200mA or 1A5-3000% in 1% stepsTime Multiplier0.05-999 in 0.01 stepsMinimum time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for definite type char.60-60000ms in 1ms stepsReset time delay for the inverse char.60-60000ms in 1ms stepsVoltage dependent overcurrent protection (51V)OperationOff, OnRestr. ModeRestrained, ControlledStart Current20-300% in 1% stepsU_lowlimit20-60% in 1% stepsU_lowlimit20-60% in 1% stepsU_lowlimit20-60% in 1% stepsU_lighlimit60-110% in 1% stepsU_lighlimit20-60% in 1% stepsDefinite time overvoltage protection (59)Definite time overvoltage protection functionStart Voltage30-130% in 1% stepsTime DelayOff, OnStart Voltage0ff, OnReste time1-60000ms in 1ms stepsCapacitor overvoltage protection functionStart Voltage0ff, OnReste time1-60000ms in 1ms stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsWarning Delay0.2-2 in 0.01 stepsRestul Overvoltage protection (59K)Operation0ff, OnStart Voltage0-2-2 in 0.01 stepsReset time1-60000ms in 1ms stepsWarning Delay0.2-2 in 0.01 stepsDiperation0ff, OnStart Voltage2-60% in 1% stepsOperation0ff | Start current | |
| In = 200mA or 1A5-3000% in 1% stepsTime Multiplier0.05-999 in 0.01 stepsMinimum time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for the inverse char.40-60000ms in 1ms stepsReset time delay for the inverse char.60-6000ms in 1ms stepsVoltage dependent overcurrent protection VVVOperationOff, OnRestr. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU_lowlimit60-110% in 1% stepsU_lwilmit60-110% in 1% stepsL_lighlimit60-110% in 1% stepsIk_limit20-60% in 1% stepsTime delay0OperationOff, OnStart Voltage30-130% in 1% stepsIk_limit20-6000ms in 1ms stepsDefinite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsTime DelayOff, OnStart Voltage protection functionCapacitor overvoltage protection function5-120% in 1% stepsNaming Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3800ms in 1ms stepsWarning Delay1-3800ms in 1ms stepsVarning Delay0-100 stepsOperation0ff, OnStart Voltage0-2-2 in 0.01 stepsWarning Delay1-3800ms in 1ms stepsOperation0ff, OnStart Voltage0-100 stepsWarning Delay1-3800ms in 1ms stepsOperation0ff, On | In = 200mA or 1A5-3000% in 1% stepsTime Multiplier0.05-999 in 0.01 stepsMinimum time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for definite type char.60-60000ms in 1ms stepsReset time delay for definite type char.60-60000ms in 1ms stepsVoltage dependent overcurrent protection (51V)OperationOff, OnRestr. ModeRestrained, ControlledStart Current20-300% in 1% stepsU_lowlimit20-60% in 1% stepsU_lowlimit20-60% in 1% stepsU_lowlimit20-60% in 1% stepsU_lighlimit60-110% in 1% stepsU_lighlimit20-60% in 1% stepsDefinite time overvoltage protection (59)Definite time overvoltage protection functionStart Voltage30-130% in 1% stepsTime DelayOff, OnStart Voltage0ff, OnRested time1-f00% in 1% stepsTime DelayOff, OnStart Voltage0ff, OnRested time1-f0000ms in 1ms stepsDiperationOff, OnStart Voltage0ff, OnRested time1-f0000ms in 1ms stepsOperation0ff, OnRested time1-60000ms in 1ms stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-8000ms in 1ms stepsWarning Delay0.2-2 in 0.01 stepsOperation0ff, OnStart Voltage2-60% in 1% stepsDiperation0ff, OnStart Voltag | | 5-3000% in 1% steps |
| Time Multiplier0.05-999 in 0.01 stepsMinimum time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for definite type char.40-60000ms in 1ms stepsReset time delay for the inverse char.60-60000ms in 1ms stepsVoltage dependent overcurrent protectionVoltage dependent overcurrent protectionVoltage dependent overcurrent protectionVoltage dependent overcurrent protectionOperationOff, OnRestr. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU_lowimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsIk_limit20-60% in 1% stepsTime delay0ff, OnStart Voltage30-130% in 1% stepsReset Ratio1-10% in 1% stepsTime Delay0ff, OnRated CurrentStart VoltageWarning Start80-120% in 1% stepsNaming Delay-60000ms in 1ms stepsWarning Delay1-60000ms in 1ms stepsWarning Delay0.22 in 0.01 stepsYarning Delay0ff, OnStart Voltage0.22 in 0.01 stepsReset time1-60000ms in 1ms stepsWarning Delay0.22 in 0.01 stepsYarning Delay0ff, OnStart Voltage0.22 in 0.01 stepsYarning Delay0ff, OnYarning Delay0ff, OnYarning Delay0ff, OnYarning Delay0ff, OnYarning Delay0.22 in 0.01 stepsYarning Delay0ff, OnYarning Delay0.260% in 1ms step | Time Multiplier0.05-999 in 0.01 stepsMinimum time delay for the inverse char.40-60000ms in 1ms stepsDefinite time delay for definite type char.40-60000ms in 1ms stepsReset time delay for the inverse char.60-60000ms in 1ms stepsVoltage dependent overcurrent protection (51V)OperationOff, OnReset T. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU_lowlimit20-60% in 1% stepsDefinite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsReset Ratio1-10% in 1% stepsTime DelayOff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsWarning Delay1-3600ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsReset time1-60000ms in 1ms stepsReset time1-60000ms in 1ms stepsCapacitor overvoltage protection functionStart Voltage0-2-2 in 0.01 stepsReset time1-60000ms in 1ms stepsCapacitor overvoltage protection functionStart Voltage0-2-2 in 0.01 stepsWarning Delay1-3600ms in 1ms stepsCapacitor overvoltage protection (59V)Operation0ff, OnReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms steps <t< td=""><td></td><td></td></t<> | | |
| Minimum time delay for the inverse char.40-60000ms in 1ms steps 40-60000ms in 1ms steps 60-60000ms in 1ms stepsDefinite time delay for the inverse char.40-60000ms in 1ms steps 60-60000ms in 1ms stepsVoltage dependent overcurrent protection VOperationOff, On Restrained, Controlled 20-3000% in 1% stepsL_lowlimit20-60% in 1% stepsU_lowlimit20-60% in 1% stepsU_lowlimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsK_limit20-6000 ms in 1ms stepsDefinite time overvoltage protection (59)OperationOff, On 30-130% in 1% stepsDefinite time overvoltage protection functionStart Voltage30-130% in 1% stepsReset Ratio1-10% in 1% stepsTime DelayOff, On 15-120% in 1% stepsOperationOff, On 30-130% in 1% stepsReset time1-60000ms in 1ms stepsCapacitor overvoltage protection functionStart VoltageReset time0-6000 ms in 1ms stepsOperationOff, On 15-120% in 1% stepsReset time1-60000 ms in 1ms stepsVarning Start80-120% in 1% stepsWarning Delay1-3600 ms in 1ms stepsVarning Delay0.2-2 in 0.01 stepsTime MultiplierOff, On 2-20% in 1% stepsReset time1-60000 ms in 1ms stepsVarning Delay1-3600 ms in 1ms stepsTime Multiplier0-10.01 stepsOperationOff, On 2-26% in 1% steps | Minimum time delay for the inverse char.40-60000ms in 1ms steps 40-60000ms in 1ms steps 60-60000ms in 1ms stepsDefinite time delay for the inverse char.40-60000ms in 1ms steps 60-60000ms in 1ms stepsVoltage dependent overcurrent protection (51V)OperationOff, On Restrained, Controlled 20-3000% in 1% stepsQuiwinit20-60% in 1% steps 20-60% in 1% stepsU_lowlimit20-60% in 1% steps 20-60% in 1% stepsU_lowlimit20-60% in 1% steps 20-60% in 1% stepsU_lowlimit20-60% in 1% steps 20-60% in 1% stepsDefinite time overvoltage protection (59)DoperationOff, On 30-130% in 1% stepsStart Voltage30-130% in 1% stepsDime Delay0-60000ms in 1ms stepsCapacitor overvoltage protection function(59C)OperationOff, On 15-120% in 1% stepsReset time16-0000ms in 1ms stepsCapacitor overvoltage protection function(59C)OperationOff, On 15-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay0-22 in 0.01 stepsTime Multiplier0.2-2 in 0.01 stepsOperationOff, On 2-60% in 1% stepsResidual overvoltage protection (59N)OperationOff, On 2-60% in 1% stepsReset time1-60000ms in 1ms stepsTime Multiplier0-60000ms in 1ms stepsDiperationOff, On 2-60% in 1% stepsResidual overvoltage protection (59N) | Time Multiplier | |
| Definite time delay for definite type char.40-60000ms in 1ms steps 60-60000ms in 1ms stepsVoltage dependent overcurrent protection (>1V)OperationOff, On Restr. ModeRestr. ModeRestrained, ControlledStart Current20-600% in 1% stepsU_lowimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsIk_limit20-60% in 1% stepsOperationOff, On Start CurrentDefinite time overvoltage protection (59)OperationOff, On Start VoltageCapacitor overvoltage protection functionOperationOff, On Start VoltageReset Ratio1-10% in 1% stepsTime DelayOff, On Start VoltageCapacitor overvoltage protection functionOperationOff, On Start VoltageReset Ratio1-10% in 1% stepsTime DelayOff, On Start VoltageReset Ratio1.5120% in 1% stepsCapacitor overvoltage protection functionUning Start80-120% in 1% stepsVarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsOperationOff, On Start Voltage | Definite time delay for definite type char. Reset time delay for the inverse char.40-60000ms in 1ms steps 60-60000ms in 1ms stepsVoltage dependent overcurrent protection (51V)Operation Restr. ModeOff, On Restrained, ControlledStart Current U_lowlimit20-60% in 1% steps 20-60% in 1% stepsU_lowlimit Ik_limit20-60% in 1% steps 20-60% in 1% stepsU_highlimit ime delay60-110% in 1% steps 0-60000ms in 1ms stepsDefinite time overvoltage protection (59)Doperation Start VoltageOff, On 30-130% in 1% stepsCapacitor overvoltage protection function time DelayOff, On 0.60000ms in 1ms stepsCapacitor overvoltage protection function time DelayOff, On 15-120% in 1% stepsCapacitor overvoltage protection function time DelayOff, On 15-120% in 1% stepsReset time warning Start time Delay0.6000ms in 1ms stepsDeperation Reset time time DelayOff, On 1-3600ms in 1ms stepsCapacitor overvoltage protection function time Delay0.2-2 in 0.01 stepsCapacitor overvoltage protection (59V)0.2-2 in 0.01 stepsOperation Reset time time DelayOff, On 2-60% in 1% stepsReset time time Delay0.2-2 in 0.01 stepsDoperation Rest time DelayOff, On 2-60% in 1% stepsCapacitor overvoltage protection (59V)0.2-2 in 0.01 stepsOperation Reset time time DultiplierOff, On 2-60% in 1% stepsCapacitor overvoltage protection (59V)0.2-2 in 0.01 stepsOperation Deperation Deperation | - | |
| Reset time delay for the inverse char.60-60000ms in 1ms stepsVoltage dependent overcurrent protectionOperationOff, OnRestr. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU_lowlimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsIk_limit20-60% in 1% stepsTime delay0-60000ms in 1ms stepsDefinite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsReset Ratio1-10% in 1% stepsTime Delay0-60000ms in 1ms stepsOperationOff, OnRated Current15-120% in 1% stepsVarning Start80-120% in 1% stepsWarning Start1-60000ms in 1ms stepsWarning Delay1-36000ms in 1ms stepsWarning Delay0.2-2 in 0.01 stepsTime Multiplier0.2-2 in 0.01 stepsOperation0ff, OnStart Voltage0.50000ms in 1ms steps | Reset time delay for the inverse char. 60-60000ms in 1ms steps Voltage dependent overcurrent protection (51V) Operation Off, On Restr. Mode Restrained, Controlled Start Current 20-3000% in 1% steps U_lowlimit 20-60% in 1% steps U_highlimit 60-110% in 1% steps U_highlimit 20-60% in 1% steps U_highlimit 20-60% in 1% steps Water and the endergy 0-60000ms in 1ms steps Definite time overvoltage protection (59) 0 Operation Off, On Start Voltage 30-130% in 1% steps Reset Ratio 1-10% in 1% steps Time Delay 0-60000ms in 1ms steps Operation Off, On Rated Current 15-120% in 1% steps Warning Start 80-120% in 1% steps Warning Start 160000ms in 1ms steps Warning Delay 0.2-2 in 0.01 steps Warning Delay 0.2-2 in 0.01 steps Residual overvoltage protection (59N) Off, On Start Voltage 2.60% in 1% steps Time Multiplier 0.2-2 in 0.01 steps Operation <t< td=""><td>-</td><td>·</td></t<> | - | · |
| OperationOff, OnRestr. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU_lowlimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsU_highlimit20-60% in 1% stepsU_highlimit20-60% in 1% stepsIk_limit20-60% in 1% stepsTime delay0-60000ms in 1ms stepsDefinite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsReset Ratio1-10% in 1% stepsTime Delay0-60000ms in 1ms stepsCapacitor overvoltage protection function (59C)OperationOff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsWarning Delay1-60000ms in 1ms stepsWarning Delay0.2-2 in 0.01 stepsTime Multiplier0.2-2 in 0.01 stepsOperationOff, OnStart Voltage0-60000ms in 1ms steps | OperationOff, OnRestr. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU_lowlimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsUk, limit20-60% in 1% stepsU_highlimit20-60% in 1% stepsU_highlimit20-60% in 1% stepsDefinite time overvoltage protection (59)Definite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsTime Delay0-60000ms in 1ms stepsCapacitor overvoltage protection function (59C)OperationOff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsResidual overvoltage protection (59N)OperationOff, OnStart Voltage0-60000ms in 1ms stepsTime Delay0-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsResidual overvoltage protection (59N)OperationOff, OnStart Voltage2-60% in 1% stepsTime Delay0-60000ms in 1ms steps | | |
| Restr. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU_lowlimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsIk_limit20-60% in 1% stepsTime delay0-60000ms in 1ms stepsDefinite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsTime Delay0-60000ms in 1ms stepsCapacitor overvoltage protection function59COperationOff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0ff, OnStart Voltage1-3600ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0ff, OnStart Voltage0.52 in 0.01 stepsCapacitor vervoltage protection (59N)0.52 in 0.01 steps | Restr. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU_lowlimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsIk_limit20-60% in 1% stepsIk_limit20-60% in 1% stepsTime delay0-60000ms in 1ms stepsDefinite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsReset Ratio1-10% in 1% stepsTime Delay0-60000ms in 1ms stepsCapacitor overvoltage protection function(59C)OperationOperationOff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1.3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsOperationOff, OnStart Voltage2-60% in 1% stepsResidual overvoltage protection (59N)OperationOff, OnStart Voltage2-60% in 1% stepsTime Delay0-60000ms in 1ms steps | Voltage dependent overcurrent protection | n (51V) |
| Restr. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU_lowlimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsIk_limit20-60% in 1% stepsTime delay0-60000ms in 1ms stepsDefinite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsTime Delay0-60000ms in 1ms stepsCapacitor overvoltage protection function59COperationOff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsVarning Delay1-3600ms in 1ms stepsTime Multiplier0ff, OnStart Voltage1-3600ms in 1ms stepsVarning Delay1-3600ms in 1ms stepsVarning Delay1-3600ms in 1ms stepsTime Multiplier0ff, OnStart Voltage2-2 in 0.01 stepsCapaciton0ff, OnStart Voltage2-60% in 1% steps | Restr. ModeRestrained, ControlledStart Current20-3000% in 1% stepsU_lowlimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsIk_limit20-60% in 1% stepsIk_limit20-60% in 1% stepsTime delay0-60000ms in 1ms stepsDefinite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsReset Ratio1-10% in 1% stepsTime Delay0-60000ms in 1ms stepsCapacitor overvoltage protection function(59C)OperationOperationOff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1.3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsOperationOff, OnStart Voltage2-60% in 1% stepsResidual overvoltage protection (59N)OperationOff, OnStart Voltage2-60% in 1% stepsTime Delay0-60000ms in 1ms steps | Operation | Off On |
| Start Current20-3000% in 1% stepsU_lowlimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsIk_limit20-60% in 1% stepsTime delay0-60000ms in 1ms stepsDefinite time overvoltage protection (59)Operation0ff, OnStart Voltage30-130% in 1% stepsReset Ratio1-10% in 1% stepsTime Delay0-60000ms in 1ms stepsOperation0ff, OnStart Voltage protection function0-60000ms in 1ms stepsOperation0ff, OnStart Voltage protection function0-60000ms in 1ms stepsOperation0ff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsOperation0ff, OnStart Voltage0-5000ms in 1ms stepsTime Multiplier0-6000ms in 1ms stepsOperation0-2-2 in 0.01 stepsStart Voltage0-6000ms in 1% stepsStart Voltage0-6000ms in 1% steps | Start Current20-3000% in 1% stepsU_lowlimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsIk_limit20-60% in 1% stepsTime delay0-60000ms in 1ms stepsDefinite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsReset Ratio1-10% in 1% stepsTime Delay0-60000ms in 1ms stepsCapacitor overvoltage protection function59COperationOff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsOperationOff, OnStart Voltage-2-60% in 1% steps | • | |
| U_lowlimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsIk_limit20-60% in 1% stepsTime delay0-60000ms in 1ms stepsDefinite time overvoltage protection (59)Operation0ff, OnStart Voltage30-130% in 1% stepsReset Ratio1-10% in 1% stepsTime Delay0-60000ms in 1ms stepsOperation0ff, OnStart Voltage protection function59C)Operation0ff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsOperation0ff, OnStart Voltage protection (59N)0.2-60% in 1% steps | U_lowlimit20-60% in 1% stepsU_highlimit60-110% in 1% stepsU_highlimit0-6000ms in 1% stepsIk_limit20-60% in 1% stepsTime delay0-6000ms in 1ms stepsDefinite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsReset Ratio1-10% in 1% stepsTime Delay0-60000ms in 1ms stepsCapacitor overvoltage protection function(59C)OperationOff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, OnOperation0ff, OnStart Voltage2-60% in 1% stepsResidual overvoltage protection (59N)0ff, OnDirection0ff, OnStart Voltage-2-60% in 1% stepsTime Delay0-60000ms in 1ms steps | | |
| U_highlimit Ik_limit Time delay60-110% in 1% steps 20-60% in 1% steps 0-60000ms in 1ms stepsDefinite time overvoltage protection (59)Off, On 30-130% in 1% steps 1-10% in 1% steps 0-60000ms in 1ms stepsOperation Start Voltage Reset Ratio Time DelayOff, On 30-130% in 1% steps 0-60000ms in 1ms stepsOperation Rated Current Warning Start Reset time Nurring DelayOff, On 15-120% in 1% steps 1-60000ms in 1ms stepsOperation Rated Current Warning Delay Time MultiplierOff, On 1-60000ms in 1ms steps 0-2-2 in 0.01 stepsOperation Residual overvoltage protection (59K)Off, On 2-60% in 1% stepsReset time Nurring Delay Time MultiplierOff, On 2-60% in 1% stepsReset time Yarning Delay Time MultiplierOff, On 2-2 in 0.01 stepsOperation Current Yarning Delay Time MultiplierOff, On 2-60% in 1% stepsOperation Start VoltageOff, On 2-60% in 1% steps | U_highlimit60-110% in 1% stepsIk_limit20-60% in 1% stepsTime delay0-60000ms in 1ms stepsDefinite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsReset Ratio1-10% in 1% stepsTime Delay0-60000ms in 1ms stepsOperationOff, OnStart Voltage protection function (59C)OperationOff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, OnOperationOff, OnStart Voltage0-60000ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, OnOperationOff, OnStart Voltage2-60% in 1% stepsTime Delay0-60000ms in 1ms steps | U lowlimit | |
| Ik_limit Time delay20-60% in 1% steps 0-6000ms in 1ms stepsDefinite time overvoltage protection (59)Off, On 30-130% in 1% steps 1-10% in 1% steps 0-6000ms in 1ms stepsOperation Start Voltage Reset Ratio Time DelayOff, On 30-6000ms in 1ms steps 0-6000ms in 1ms stepsCapacitor overvoltage protection function Varning Start Reset time Warning Delay Time MultiplierOff, On 15-120% in 1% steps 1-6000ms in 1ms steps 0-2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, On 2-60% in 1% steps | Ik_limit20-60% in 1% steps 0-60000ms in 1ms stepsDefinite time overvoltage protection (59)OperationOff, On 30-130% in 1% steps 1-10% in 1% steps 0-60000ms in 1ms stepsReset Ratio1-10% in 1% steps 0-60000ms in 1ms stepsTime DelayOff, On 0-60000ms in 1ms stepsOperationOff, On 15-120% in 1% steps 0-60000ms in 1ms stepsOperation Rated CurrentOff, On 15-120% in 1% steps 80-120% in 1% steps 0-22 in 0.01 stepsWarning Start Reset time1-60000ms in 1ms steps 0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, On 0.2-2 in 0.01 stepsOperation Rated Voltage Time DelayOff, On 0.2-2 in 0.01 steps | | |
| Definite time overvoltage protection (59)OperationOff, OnStart Voltage30-130% in 1% stepsReset Ratio1-10% in 1% stepsTime Delay0-60000ms in 1ms stepsCapacitor overvoltage protection function (59C)OperationOff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsOperationOff, OnStart VoltageOff, OnStart Voltage0.1% stepsOperation0.2-2 in 0.01 steps | Definite time overvoltage protection (59)OperationOff, On 30-130% in 1% steps 1-10% in 1% steps 0-60000ms in 1ms stepsReset Ratio1-10% in 1% steps 0-60000ms in 1ms stepsCapacitor overvoltage protection function (59C)OperationOff, On 15-120% in 1% steps 80-120% in 1% steps 1-60000ms in 1ms stepsQuerationOff, On 15-120% in 1% steps 1-60000ms in 1ms stepsWarning Start80-120% in 1% steps 1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms steps 0.2-2 in 0.01 stepsCoperationOff, On 2-60% in 1% steps 0.2-2 in 1% stepsResidual overvoltage protection (59N)Off, On 2-60% in 1% steps 0-60000ms in 1ms steps | | |
| Operation Start VoltageOff, On 30-130% in 1% stepsReset Ratio Time Delay1-10% in 1% steps 0-60000ms in 1ms stepsCapacitor overvoltage protection function (59C)Operation Rated Current Warning Start Reset timeOff, On 15-120% in 1% steps 80-120% in 1% steps 1-60000ms in 1ms stepsWarning Delay Time Multiplier1-60000ms in 1ms steps 0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Operation Capacitor overvoltage protection (59N)Operation Capacitor overvoltage protection (59N)Operation Capacitor overvoltageOperation Capacitor overvoltage protection (59N)Operation Capacitor overvoltageOperation Capacitor overvoltageOperation Capacitor overvoltageOperation Start VoltageOff, On Start VoltageOperation Start VoltageOperation Start VoltageOperation Start VoltageOperation Start VoltageOperation Start VoltageOperation Start VoltageOperation Start Voltage | Operation Start Voltage Reset RatioOff, On 30-130% in 1% steps 1-10% in 1% steps 0-60000ms in 1ms stepsCapacitor overvoltage protection function (59C)Operation Rated CurrentOff, On 15-120% in 1% steps 80-120% in 1% steps 80-120% in 1% steps 1.3600ms in 1ms stepsWarning Start Reset time1-60000ms in 1ms steps 1.3600ms in 1ms stepsWarning Delay Time Multiplier0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, On 2-60% in 1% steps 0-60000ms in 1ms stepsOperation Residual overvoltage protection (59N)Off, On 2-60% in 1% steps 0-60000ms in 1ms steps | Time delay | 0-60000ms in 1ms steps |
| Start Voltage Reset Ratio30-130% in 1% steps 1-10% in 1% steps 0-60000ms in 1ms stepsCapacitor overvoltage protection function59COperation Rated CurrentOff, On 15-120% in 1% steps 80-120% in 1% steps 1-60000ms in 1ms stepsWarning Start Reset time Warning Delay Time Multiplier1-60000ms in 1ms steps 1-60000ms in 1ms steps 0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, On 2-60% in 1% steps | Start Voltage Reset Ratio30-130% in 1% steps 1-10% in 1% steps 0-60000ms in 1ms stepsCapacitor overvoltage protection function(59C)Operation Rated CurrentOff, On 15-120% in 1% steps 80-120% in 1% stepsWarning Start Reset time1-60000ms in 1ms stepsWarning Delay Time Multiplier1-3600ms in 1ms steps 0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, On 2-60% in 1% steps 0.2-2 in 0.00 ms in 1ms stepsOperation Coperation CoperationOff, On 0.2-2 in 0.01 steps | Definite time overvoltage protection (59) | |
| Start Voltage Reset Ratio30-130% in 1% steps 1-10% in 1% steps 0-60000ms in 1ms stepsCapacitor overvoltage protection function59COperation Rated CurrentOff, On 15-120% in 1% steps 80-120% in 1% steps 1-60000ms in 1ms stepsWarning Start Reset time Warning Delay Time Multiplier1-60000ms in 1ms steps 1-60000ms in 1ms steps 0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, On 2-60% in 1% steps | Start Voltage Reset Ratio30-130% in 1% steps 1-10% in 1% steps 0-60000ms in 1ms stepsCapacitor overvoltage protection function(59C)Operation Rated CurrentOff, On 15-120% in 1% steps 80-120% in 1% stepsWarning Start Reset time1-60000ms in 1ms stepsWarning Delay Time Multiplier1-3600ms in 1ms steps 0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, On 2-60% in 1% steps 0.2-2 in 0.00 ms in 1ms stepsOperation Coperation CoperationOff, On 0.2-2 in 0.01 steps | Operation | Off, On |
| Reset Ratio Time Delay1-10% in 1% steps 0-60000ms in 1ms stepsCapacitor overvoltage protection function59COperation Rated CurrentOff, On 15-120% in 1% steps 80-120% in 1% stepsWarning Start Reset time80-120% in 1% steps 1-60000ms in 1ms stepsWarning Delay Time Multiplier1-3600ms in 1ms steps 0.2-2 in 0.01 stepsReseidual overvoltage protection (59N)Off, On 2-60% in 1% steps | Reset Ratio1-10% in 1% steps 0-60000ms in 1ms stepsCapacitor overvoltage protection function (59C)OperationOff, On 15-120% in 1% steps 80-120% in 1% stepsWarning Start80-120% in 1% steps 1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms steps 0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, On 2-60% in 1% steps 0.2-20 in 0.01 stepsOperation Start Voltage Time DelayOff, On 2-60% in 1% steps 0-6000ms in 1ms steps | - | |
| Capacitor overvoltage protection function (59C)Operation Rated CurrentOff, On 15-120% in 1% stepsWarning Start Reset time80-120% in 1% stepsWarning Delay Time Multiplier1-60000ms in 1ms steps 0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, On 2-60% in 1% steps | Capacitor overvoltage protection function (59C)OperationOff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsOperationOperationOff, OnStart Voltage2-60% in 1% stepsTime Delay0-60000ms in 1ms steps | 0 | |
| OperationOff, OnRated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsOperationOperationOff, OnStart Voltage0.4-00% in 1% steps | Operation Rated CurrentOff, On 15-120% in 1% stepsWarning Start Reset time80-120% in 1% stepsWarning Delay Time Multiplier1-60000ms in 1ms stepsResidual overvoltage protection (59N)0.2-2 in 0.01 stepsOperation Start Voltage Time DelayOff, On 2-60% in 1% stepsOperation Start Voltage Time Delay0.50000ms in 1ms steps | Time Delay | 0-60000ms in 1ms steps |
| Rated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsOperationOperationOff, On 2-60% in 1% steps | Rated Current15-120% in 1% stepsWarning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsResidual overvoltage protection (59N)OperationOff, OnStart Voltage2-60% in 1% stepsTime Delay0-60000ms in 1ms steps | Capacitor overvoltage protection function | n (59C) |
| Warning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsResidual overvoltage protection (59N)OperationOff, On 2-60% in 1% steps | Warning Start80-120% in 1% stepsReset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsOperationOff, OnStart Voltage2-60% in 1% stepsTime Delay0-60000ms in 1ms steps | Operation | Off, On |
| Reset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsResidual overvoltage protection (59N)OperationOff, OnStart Voltage2-60% in 1% steps | Reset time1-60000ms in 1ms stepsWarning Delay1-3600ms in 1ms stepsTime Multiplier0.2-2 in 0.01 stepsResidual overvoltage protection (59N)OperationOff, OnStart Voltage2-60% in 1% stepsTime Delay0-60000ms in 1ms steps | | |
| Warning Delay Time Multiplier1-3600ms in 1ms steps 0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, On 2-60% in 1% steps | Warning Delay Time Multiplier1-3600ms in 1ms steps 0.2-2 in 0.01 stepsResidual overvoltage protection (59N)Off, OnOperation Start Voltage Time DelayOff, On 2-60% in 1% steps 0-60000ms in 1ms steps | Warning Start | 80-120% in 1% steps |
| Time Multiplier 0.2-2 in 0.01 steps Residual overvoltage protection (59N) Operation Off, On Start Voltage 2-60% in 1% steps | Time Multiplier 0.2-2 in 0.01 steps Residual overvoltage protection (59N) Operation Off, On Start Voltage 2-60% in 1% steps Time Delay 0-60000ms in 1ms steps | Reset time | 1-60000ms in 1ms steps |
| Residual overvoltage protection (59N) Operation Off, On Start Voltage 2-60% in 1% steps | Residual overvoltage protection (59N) Operation Off, On Start Voltage 2-60% in 1% steps Time Delay 0-60000ms in 1ms steps | Warning Delay | 1-3600ms in 1ms steps |
| Operation Off, On Start Voltage 2-60% in 1% steps | OperationOff, OnStart Voltage2-60% in 1% stepsTime Delay0-60000ms in 1ms steps | Time Multiplier | 0.2-2 in 0.01 steps |
| Start Voltage 2-60% in 1% steps | Start Voltage 2-60% in 1% steps Time Delay 0-60000ms in 1ms steps | Residual overvoltage protection (59N) | |
| | Time Delay 0-60000ms in 1ms steps | Operation | Off, On |
| Time Delay 0-60000ms in 1ms steps | | Start Voltage | 2-60% in 1% steps |
| | | Time Delay | 0-60000ms in 1ms steps |



| Operation | Off, On |
|--|---|
| Start Signal Only | False, True |
| Start Current Diff | 10-90% in 1% steps |
| Time Delay | 100-60000ms in 1ms steps |
| Voltage transformer supervision (60) | |
| Operation | Off, Zero sequence, Negative sequence, Special |
| Min Operate Voltage | 10-100% in 1% steps |
| Min Operate Current | 2-100% in 1% steps |
| Start URes | 5-50% in 1% steps |
| Start IRes | 10-50% in 1% steps |
| Start UNeg | 5-50% in 1% steps |
| Start INeg | 10-50% in 1% steps |
| Three-phase directional overcurrent prote | ction (67) |
| Direction | NonDir, Forward, Backward |
| Operation | Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC |
| | Longinv, ANSI Inv, ANSI Modinv, ANSI Veryinv, ANSI Extinv, |
| | ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv |
| Operating Angle | 30-80° in 1° steps |
| Characteristic Angle | 40-90° in 1° steps |
| Start Current | 5-3000% in 1% steps |
| Time Multiplier | 0.05-999 in 0.01 steps |
| Minimum time delay for the inverse char. | 30-60000ms in 1ms steps |
| Definite time delay | 30-60000ms in 1ms steps |
| Reset time | 60-60000ms in 1ms steps |
| | |
| Residual directional overcurrent protection | n (67N/67Ns) |
| Residual directional overcurrent protection | |
| Residual directional overcurrent protection | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), |
| | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), |
| Direction | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) |
| | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC |
| Direction | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, |
| Direction | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv |
| Direction Operation Start Current | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps |
| Direction Operation Start Current URes Min | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 1-20% in 1% steps |
| Direction Operation Start Current URes Min IRes Min | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 1-20% in 1% steps 1-50% in 1% steps |
| Direction Operation Start Current URes Min IRes Min Operating Angle | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 1-20% in 1% steps 30-85° in 1° steps |
| Direction Operation Start Current URes Min IRes Min Operating Angle Characteristic Angle | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 1-20% in 1% steps 1-50% in 1% steps 30-85° in 1° steps -180-180° in 1° steps |
| Direction Operation Start Current URes Min IRes Min Operating Angle Characteristic Angle Time Multiplier | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 1-20% in 1% steps 1-50% in 1% steps 30-85° in 1° steps -180-180° in 1° steps 0.05-999 in 0.01 step |
| Direction Operation Start Current URes Min IRes Min Operating Angle Characteristic Angle Time Multiplier Minimal time delay for the inverse char. | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 1-20% in 1% steps 1-50% in 1% steps 30-85° in 1° steps -180-180° in 1° steps 0.05-999 in 0.01 step 30-60000ms in 1ms steps |
| Direction Operation Start Current URes Min IRes Min Operating Angle Characteristic Angle Time Multiplier | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 1-20% in 1% steps 1-50% in 1% steps 30-85° in 1° steps -180-180° in 1° steps 0.05-999 in 0.01 step |
| Direction Operation Start Current URes Min IRes Min Operating Angle Characteristic Angle Time Multiplier Minimal time delay for the inverse char. Definite time delay | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 1-20% in 1% steps 1-50% in 1% steps 30-85° in 1° steps -180-180° in 1° steps 0.05-999 in 0.01 step 30-60000ms in 1ms steps 30-60000ms in 1ms steps |
| Direction Operation Start Current URes Min IRes Min Operating Angle Characteristic Angle Time Multiplier Minimal time delay for the inverse char. Definite time delay Reset time delay for the inverse char. | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 1-20% in 1% steps 1-20% in 1% steps 30-85° in 1° steps -180-180° in 1° steps 0.05-999 in 0.01 step 30-60000ms in 1ms steps 30-60000ms in 1ms steps 30-60000ms in 1ms steps |
| Direction Operation Start Current URes Min IRes Min Operating Angle Characteristic Angle Time Multiplier Minimal time delay for the inverse char. Definite time delay Reset time delay for the inverse char. Inrush detection (68) Operation | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 1-20% in 1% steps 1-20% in 1% steps 30-85° in 1° steps -180-180° in 1° steps 0.05-999 in 0.01 step 30-60000ms in 1ms steps 30-60000ms in 1ms steps 30-60000ms in 1ms steps 0.05f, On |
| Direction Operation Start Current URes Min IRes Min Operating Angle Characteristic Angle Time Multiplier Minimal time delay for the inverse char. Definite time delay Reset time delay for the inverse char. | NonDir, Forward - Angle, Backward Angle, Forward I*cos(fi), Backward- Angle, Forward-I*cos(fi), Backward - I*sin(fi), Forward-I*sin(fi+45), Backward - I*sin(fi+45) Off, DefiniteTime, IEC Inv,IEC VeryInv, IEC ExtInv,IEC LongInv, ANSI Inv, ANSI ModInv, ANSI VeryInv, ANSI ExtInv, ANSI LongInv, ANSI LongVeryInv, ANSI LongExtInv 5-3000% in 1% steps 1-20% in 1% steps 1-20% in 1% steps 30-85° in 1° steps -180-180° in 1° steps 0.05-999 in 0.01 step 30-60000ms in 1ms steps 30-60000ms in 1ms steps 30-60000ms in 1ms steps |

•







| Operation | Off, On | |
|--|--|--|
| PhaseDiff Limit | 5-25deg in 1deg steps | |
| Max NegSeq Voltage | 10-100% in 1% steps | |
| Time Delay | 5-50% in 1% steps | |
| Max ZeroSeq Voltage | 1-30% in 1% steps | |
| Pulse Duration | 150-500ms in 1ms steps | |
| Auto-reclose (79) | | |
| Operation | Off, On | |
| EarthFault RecCycle | Disabled, 1. Enabled, 1.2. Enabled, 1.2.3. Enabled, 1.2.3.4. | |
| | Enabled | |
| PhaseFault RecCycle | Disabled, 1. Enabled, 1.2. Enabled, 1.2.3. Enabled, 1.2.3.4. | |
| | Enabled | |
| Reclosing Started by | Trip reset, CB open | |
| 1. Dead Time Ph | 0-100000ms in 10ms steps | |
| 2. Dead Time Ph | 10-100000ms in 10ms steps | |
| 3. Dead Time Ph | 10-100000ms in 10ms steps | |
| 4. Dead Time Ph | 10-100000ms in 10ms steps | |
| 1. Dead Time EF | 0-100000ms in 10ms steps | |
| 2. Dead Time EF | 10-100000ms in 10ms steps | |
| 3. Dead Time EF | 10-100000ms in 10ms steps | |
| 4. Dead Time EF | 10-100000ms in 10ms steps | |
| Reclaim Time | 100-300000ms in 10ms steps | |
| Close Command Time | 10-10000ms in 10ms steps | |
| Dynamic Blocking Time | 10-100000ms in 10ms steps | |
| Block after Man Close | 0-100000ms in 10ms steps | |
| Action Time | 0-20000ms in 10ms steps | |
| Start Signal Max Time | 0-10000ms in 10ms steps | |
| DeadTime Max Delay | 0-100000ms in 10ms steps | |
| CB Supervision Time | 10-100000ms in 10ms steps | |
| SynCheck Max Time | 500-100000ms in 10ms steps | |
| SynCheck Max Time | 500-100000ms in 10ms steps | |
| CB State Monitoring | False, True | |
| Accelerate 1.Trip | False, True | |
| Accelerate 2.Trip | False, True | |
| Accelerate 3.Trip | False, True | |
| Accelerate 4.Trip | False, True | |
| Overfrequency protection (81O) Undefrequency protection (81U) | | |
| Operation | Off, On | |
| Start signal only | False, True | |
| Start frequency | 40-70Hz in 0.01Hz steps | |
| Time Delay | 0-60000ms in 1ms steps | |
| Voltage limit | 0.3-1.0 Un | |
| Rate of change of frequency protection (81R) | | |
| Operation | Off, On | |
| Start signal only | False, True | |
| Start df/dt | -5.00-5.00Hz/s in 0.01Hz/s steps | |
| Time Delay | 0-60000ms in 1ms steps | |
| Lockout trip logic (86/94) | | |









FUNCTIONAL PARAMETERS

| Operation Min pulse duration | Off, On, Lockout 50-60000ms in 1ms steps |
|---|---|
| Rectricted earth fault protection (87N) | |
| Operation | Off, On |
| Directional check | False, True |
| lo Primary Match | 20-500% in 1% steps |
| Neutral Match | 100-1000% in 1% steps |
| Base Sensitivity | 10-50% in 1% steps |
| Slope | 50-100% in 1% steps |
| Base Sens Bias Limit | 100-200% in 1% steps |
| Switch-onto-fault (SOTF) | |
| Operation | Off, On |
| SOTF Drop Delay | 10-10000ms in 1ms steps |
| Disturbance recorder | |
| Operation | Off, On |
| Resolution | 1/1.2 kHz; 2/2.4kHz |
| Prefault | 100-1000ms in 1ms steps |
| PostFault | 100-10000ms in 1ms steps |
| Max Recording Time | 500-10000ms in 1ms steps |









TECHNICAL DATA

| HARDWARE | | |
|---|--|--|
| Analog Inputs (Current & Voltage Input M | lodules) | |
| Rated current In Rated voltage Vn Rated frequency Overload rating | 1A or 5A (selectable) 110V (± 10%) 50Hz or 60Hz | |
| Current inputs Voltage inputs Burden | 20A continuous, 175A for 10s, 500A for 1s, 1200A for 10ms 250V continuous, 275V for 1s | |
| Phase current inputs Voltage inputs | 0.01VA at In = 1A, 0.25VA at In = 5A 0.61VA at 200V, 0.2VA at 100V | |
| Power Supply | | |
| Rated auxiliary voltage | 24/48/60VD (Operative range: 19.2 -72Vdc) 110/220Vdc (Operative range: 88 - 264Vdc or 80-250Vac) | |
| Power consumption | 20W | |
| Binary Inputs | | |
| Input circuit DC voltage Pickup voltage Drop voltage Power consumption | 24VDC (Thermal withstand voltage: 72VDC) 48VDC (Thermal withstand voltage: 100VDC) 110VDC (Thermal withstand voltage: 250VDC) 220VDC (Thermal withstand voltage: 320VDC) 0.8Un 0.64Un max. 1.6 mA per channel at 220VDC max. 1.8 mA per channel at 110VDC max. 2 mA per channel at 48VDC | |
| D'acces Outcute | max. 3 mA per channel at 24VDC | |
| Binary Outputs | | |
| Rated voltage Continuous carry Maximum switching voltage Breaking capacity Short time carrying capacity Operating time | 250VAC/DC 8A 400VAC 0.2A at 220VDC, 0.3A at 110VDC (L/R=40ms) 2000VA max 35A for 1s Typically 10ms | |
| Trip Contacts | | |
| Rated voltage Continuous carry Thermal withstand voltage | 24VDC/48VDC/110VDC/220VDC 8A 72VDC (Rated voltage: 24VDC or 48VDC) 150VDC (Rated voltage: 110VDC) 242VDC (Rated voltage: 220VDC) | |
| Breaking capacity Making capacity Operating time | 4A (L/R=40ms) 30A for 0.5s With pre-trip 0.5 ms, without pre-trip typically 10 ms | |
| Mechanical Design | | |



| Installation Case Protection class | Flush mounting 24 HP (height:3U) IP41 from front side, IP2x from rear side IP54 Rated mounting (additional gasket inserted into the original front panel frame) |
|---|--|
| Key & LED | |
| Device keys Capacitive touch key LEDS Number of configurable LED Device status LED | Capacitive touch keys 4 pcs yellow, 3 mm circular LEDs indicating touch key actions 16 1 piece three-color, 3 mm circular LED Green: normal device operation Yellow: device is in warning state Red: device is in error state |
| Local Interface | |
| Service port on front panel | 10/100-Base-T interface with RJ-45 type connector |
| System Interface | |
| 10/100-Base-TX 100Base-FX Serial Interface | IP56 rated with RJ-45 connector MM/ST 1300 nm, 50/62.5/125 μm connector, (up to 2 km) fiber MM/LC 1300 nm, 50/62.5/125 μm connector, (up to 2 km) fiber SM/FC 1550 nm, 9/125 μm connector, (up to 120 km), with max. 32 dB link attenuation SM/FC 1550 nm, 9/125 μm connector, (up to 50 km), with max. 27 dB link attenuation Plastic optical fiber (ASIF-POF) |
| | Glass with ST connector (ASIF-GS) Galvanic RS485/422 (ASIF-G) |
| PROTECTION & CONTROL FUNCTION Circuit breaker control function block | - |
| Operate time accuracy | ±5% or ±15 ms, whichever is greater |
| Disconnector control function (DisCor | |
| Operate time accuracy | ±5% or ±15 ms, whichever is greater |
| Overexcitation protection (24) | |
| Voltage measurement Frequency measurement | 0.5-1.2U _n , accuracy: <1% 0.8-1.2f _n , accuracy: <1% |
| Synchrocheck (25) | |
| Rated Voltage Un Voltage effective range | 100/200V, parameter setting 10-110 % of Un, accuracy: ±1% of Un |

47.5 – 52.5 Hz, accuracy: ±10 mHz

Setting value, accuracy: ±3 ms

±3°

<50 ms

0.95 Un

0 0

Definite time undervoltage protection (27)

Frequency

Operate time

Reset time

Reset ratio

Phase angle accuracy



| Pick-up starting accuracy | < ± 0,5 % |
|---|---|
| Reset time | |
| $U > \rightarrow Un$ | 50 ms |
| $U > \rightarrow 0$ | 40 ms |
| Operate time accuracy | < ± 20 ms |
| Minimum operate time | 50 ms |
| Directional over-power protection (32) | |
| P,Q measurement | Effective range: I>5% In, accuracy: <3% |
| Directional under-power protection (37) | |
| P,Q measurement | Effective range: I>5% In, accuracy: <3% |
| Loss-of-load (undercurrent) protection (37) | |
| Current Accuracy | ±1% of In (Range: 20-2000% of In) |
| Reset Ratio | 0.95 |
| Operating Time Accuracy | ±5% or ±15 ms, whichever is greater |
| Min. Operating Time | <60ms |
| Reset Time | <60ms |
| Negative sequence overcurrent protection | (46) |
| Operating accuracy | <2% (when 20 ≤ Gs ≤ 1000) |
| Operate time accuracy | ±5% or ±15 ms, whichever is greater |
| Reset ratio | 0.95 |
| Reset time | |
| Dependent time char. | Dependent time char. |
| Definite time char. | Approx 60 ms |
| Reset accuracy time | < 2% or ±35 ms, whichever is greater |
| Transient overreach | < 2 % |
| Pickup time * | < 40 ms |
| Overshot time | |
| Dependent time char. | 25 ms |
| Definite time char. | 45 ms |
| Influence of time varying value of the | < 4 % |
| input current (IEC 60255-151) accuracy | |
| Brocken conductor protection (46BC) | |
| Pick-up starting accuracy | <2 % |
| Reset ratio | 0.95 |
| Min. operate time | 70ms |
| Negative sequence overvoltage protection | (47) |
| Pick-up starting accuracy | < ± 0,5 % |
| Blocking voltage accuracy | < ± 1,5 % |
| Reset time | |
| U > \rightarrow Un | 60 ms |
| $U > \rightarrow 0$ | 50 ms |
| Operate time accuracy | < ± 20 ms |
| Drop-off ratio accuracy | ± 0,5 % |
| Minimum operate time | 50 ms |
| Motor startup supervision (48/66) | |
| | |



| Current Accuracy | <6% of In (Range: 20-2000% of In) |
|---|---|
| Reset Ratio | 0.95 at Startup Current |
| Operating Time Accuracy | ±5% or ±15 ms, whichever is greater |
| Reset Time | <60ms |
| Thermal protection (49) | |
| Operate time at I>1.2*Itrip accuracy | <3 % or <+ 20 ms |
| Three-phase instantaneous overcurrent pr | otection (50) |
| Using | peak value calculation |
| Operating characteristic | Instantaneous, accuracy < 6 % |
| Reset ratio | 0.85 |
| Operate time at 2*Is | <15 ms |
| Reset time | <40 ms |
| Transient overreach | 90% |
| Using Four | er basic harmonic calculation |
| Operating characteristic | Instantaneous, accuracy < 2 % |
| Reset ratio | 0.85 |
| Operate time at 2*Is | <25 ms |
| Reset time | <60 ms |
| Transient overreach | 15% |
| Breaker failure protection (50BF) | |
| Pick-up starting accuracy | <2 % |
| Operating time accuracy | ±5% or ±15 ms, whichever is greater |
| Retrip time | approx. 15 ms |
| Reset ratio | 0.9 |
| Current reset time | 16-25ms |
| | |
| Residual instantaneous overcurrent protect | ction (50N/50Ns) |
| | ction (50N/50Ns) peak value calculation |
| Using Operating characteristic (I>0.1 In) | |
| Using Operating characteristic (I>0.1 In) Reset ratio | peak value calculation Instantaneous, accuracy <6% 0.85 |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*I _S | Instantaneous, accuracy <6% 0.85 < 15 ms |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*I _S Reset time * | peak value calculation Instantaneous, accuracy <6% |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*I _S | Instantaneous, accuracy <6% 0.85 < 15 ms |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach | peak value calculation Instantaneous, accuracy <6% |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach Using Fouri Operating characteristic (I>0.1 In) | peak value calculation Instantaneous, accuracy <6% |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach Using Fouri Operating characteristic (I>0.1 In) Reset ratio | peak value calculation Instantaneous, accuracy <6% |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach Using Fouri Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is | peak value calculation Instantaneous, accuracy <6% |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach Using Fouri Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * | peak value calculation Instantaneous, accuracy <6% |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach Using Fouri Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is | peak value calculation Instantaneous, accuracy <6% |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach Using Fouri Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * | peak value calculation Instantaneous, accuracy <6% |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach Using Fouri Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach | peak value calculation Instantaneous, accuracy <6% |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach Using Fouri Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach Three-phase time overcurrent protection (Operating accuracy Operate time accuracy | peak value calculationInstantaneous, accuracy <6% |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach Using Fouri Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach Three-phase time overcurrent protection (Operating accuracy Operate time accuracy Reset ratio | peak value calculationInstantaneous, accuracy <6% |
| Using Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach Using Fouri Operating characteristic (I>0.1 In) Reset ratio Operate time at 2*Is Reset time * Transient overreach Three-phase time overcurrent protection (Operating accuracy Operate time accuracy | peak value calculationInstantaneous, accuracy <6% |

 $\left[0 \right]$



|--|--|--|--|--|--|--|--|

| Definite time char. | Approx 60 ms |
|--|--------------------------------------|
| Reset time accuracy | < 2% or ±35 ms, whichever is greater |
| Transient overreach | < 2 % |
| Pickup time * | < 40 ms |
| Overshot time | |
| Dependent time char. | 30 ms |
| Definite time char. | 50 ms |
| Influence of time varying value of the | < 4 % |
| input current (IEC 60255-151) | |
| Capacitor unbalance protection (51C) | |
| Pick-up starting accuracy | <5% |
| Pickup time | <40 ms |
| - | |
| Angle accuracy Reset ratio | <1 degree 0.9 |
| | |
| Reset time | <2% or ±35 ms, whichever is greater |
| Operate time accuracy | ±5% or ±15 ms, whichever is greater |
| Residual time overcurrent protection (51N/ | 51Ns) |
| Operating accuracy | $<3\%$ (when $20 \le G_S \le 1000$) |
| Operate time accuracy | ±5% or ±15 ms, whichever is greater |
| Reset ratio | 0.95 |
| Reset time | |
| Dependent time char. | Dependent time char. |
| Definite time char. | Approx 60 ms |
| Reset accuracy time | < 2% or ±35 ms, whichever is greater |
| Transient overreach | < 2 % |
| Pickup time * | ≤ 40 ms |
| Overshot time | S 40 ms |
| | 00 |
| Dependent time char. | 30 ms |
| Definite time char. | 50 ms |
| Influence of time varying value of the | < 4 % |
| input current (IEC 60255-151) accuracy | |
| Voltage dependent overcurrent protection | (51V) |
| Operating accuracy | $<2\%$ (when $20 \le G_S \le 1000$) |
| Operate time accuracy | ±5% or ±15 ms, whichever is greater |
| Reset ratio | 0.95 |
| Reset time | |
| Dependent time char. | Dependent time char. |
| Definite time char. | Approx 60 ms |
| Reset time accuracy | < 2% or ±35 ms, whichever is greater |
| Transient overreach | < 2 % |
| Pickup time * | < 40 ms |
| | |
| Overshot time | 00 |
| Dependent time char. | 30 ms |
| Definite time char. | 50 ms |
| Influence of time varying value of the | < 4 % |
| input current (IEC 60255-151) | |
| Definite time overvoltage protection (59) | 1 |

| | | INICAL DATA |
|---|--|-------------|
| Pick-up starting accuracy Reset time $U > \rightarrow Un$ $U > \rightarrow 0$ Operate time accuracy Minimum operate time | < ± 0,5 % 60 ms 50 ms < ± 20 ms 50 ms | |
| Capacitor overvoltage protection (59C) Pick-up starting accuracy Operate time accuracy | <1% <5% | |
| Residual overvoltage protection (59N)Pick-up starting accuracy $2-8\%$ $8-60\%$ Reset time $U> \rightarrow Un$ $U> \rightarrow 0$ Operate timeOperate time accurracy | < ± 2 % < ± 1.5 % 60 ms 50 ms 50 ms < ± 20 ms | |
| Current transformer supervision (60) Pick-up starting accuracy at In Reset ratio Operate time | Pick-up starting accuracy at In 0.95 70 ms | |
| Voltage transformer supervision (60) Pick-up voltage accuracy Operate time Reset ratio | <1% <20 ms 0.95 | |
| Three-phase directional overcurrent protect | tion (67) | |
| Operating accuracy Operating accuracy Accuracy in minimum time range Reset ratio Reset time Transient overreach Pickup time Memory storage time span 50Hz 60Hz Angular accuracy | < 2 % If Time multiplier is >0.1: ±5% or ±15 ms, whichever is greater ±35 ms 0.95 Approx 100 ms 2 % <100 ms 70 ms 60 ms <3° | |
| Residual directional overcurrent protection | | |
| Operating accuracy Operating accuracy Accuracy in minimum time range Reset ratio Reset time Reset time accuracy Transient overreach | < ±2 % ±5% or ±15 ms, whichever is greater 0.95 ±35 ms 0.95 Approx 50 ms ±35 ms < 2 % | |



| Pickup time | ±35 ms |
|--|---|
| Angular accuracy | <3° |
| lo ≤ 0.1 ln | <±10° |
| lo ≤ 0.1 In | < <u>-</u> +0 < <u>+5</u> ° |
| •••• | - |
| lo ≤ 0.1 In | <±2° |
| Angular reset ratio | |
| Forward and backward | 10° |
| All other selection | 5° |
| | |
| Inrush detection (68) | |
| Range | 20 – 2000% of In |
| Current accuracy | ±1% of In |
| - | |
| Vector jump protection (78) | |
| Pick-up starting accuracy | ±5% |
| Blocking voltage | U>0.2Un, accuracy: <5% |
| Operate time | |
| Jump>2*setting | <50 ms |
| Minimum operate time | 40 ms |
| | |
| Pulse duration | 150 500 ms, accuracy: <10 ms |
| Auto-reclose (79) | |
| Operating time accuracy | ±1% of setting value or ±30 ms |
| Overfrequency protection (810) | |
| Undefrequency protection (81U) | |
| Min. operate voltage | 0.1 Un |
| Operate range | 40 - 60 Hz (50 Hz system) |
| | 50 - 70 Hz (60 Hz system) |
| Effective range | 45 - 55 Hz (50 Hz system) |
| | 55 - 65 Hz (60 Hz system) |
| | |
| Accuracy | ± 3 mHz |
| Minimum operate time | 93ms (50 Hz system) |
| | 73ms Hz (60 Hz system) |
| Minimum operate time accuracy | ± 32 ms (50 Hz system) |
| | ± 27 ms (60 Hz system) |
| Accuracy when time delay: | |
| 140 - 60000 ms | + 4 ma |
| | ± 4 ms |
| <140 ms (50 Hz system) | ± 32 ms |
| <140 ms (60 Hz system) | ± 27 ms |
| Reset frequency | [Start freq.] – 101 mHz, accuracy: ± 1 mHz |
| Reset time | 98 ms (50 Hz) |
| | 85 ms (60 Hz) |
| Reset time accuracy | ± 6 ms |
| Rate of change of frequency protection (81 | R) |
| | 0.1 Un |
| Min. operate voltage | |
| Operate range | ± 10 Hz/s, accuracy: ± 50 mHz/s |
| Effective range | ± 5 Hz/s, accuracy: ± 15 mHz/s |
| Minimum operate time | 191 ms (50 Hz system), accuracy: ± 40 ms |
| - | 159 ms (60 Hz system), accuracy: ± 39 ms |
| | |
| | 200 - 60000 ms (50 Hz) - 300003000 + 2 ms |
| Time delay (at 0.2 Hz/s) | 200 – 60000 ms (50 Hz), accuracy: ± 2 ms ± 1 mHz |

| Reset ratio (drop/pick in absolute values) Reset time Lockout trip logic (86/94) Pulse time | 0.92 (>0.5 Hz/s), accuracy: -0.03 0.999 (<0.5 Hz/s), accuracy: -0.072 187 ms (50Hz), accuracy: ±44ms 157 ms (60Hz), accuracy: ±38 ms <3 ms |
|---|---|
| Rectricted earth fault protection (87N) | |
| Operating characteristic Reset ratio Characteristic accuracy Opera time, restrained Reset time, restrained | 1 breakpoint 0.95 <2% Typically 20ms Typically 25ms |
| Switch-onto-fault (SOTF) | |
| Timer accuracy | ±5% or ±15 ms, whichever is greater |
| MEASUREMENT FUNCTION | |
| Current With CT+/5151; CT+/5153 (Channel 1-3) With CT+/1500 Voltage With VT+/2211 Power (P,Q,S, PF) With CT+/5151; CT+/5153 (Channel 1-3) With CT+/1500 Frequency | Range: $0.05 - 20$ ln, accuracy: $\pm 0.5\%$, ± 1 digit Range: $0.02 - 2$ ln, accuracy: $\pm 0.2\%$, ± 1 digit Range: $0.05 - 1.5$ Un, accuracy: $\pm 0.5\%$, ± 1 digit Range: $0.05 - 20$ ln, accuracy: $\pm 0.5\%$, ± 1 digit Range: $0.02 - 2$ ln, accuracy: $\pm 0.2\%$, ± 1 digit Range: $40 - 60$ Hz (50 Hz system); accuracy: ± 2 mHz Range: $50 - 70$ Hz (60 Hz system); accuracy: ± 2 mHz |

۵.,

Y

ENVIRONMENTAL PERFORMANCE

000

| Atmospheric Environment | | |
|---|----------------|--|
| Temperature | IEC 60068-2-1 | Storage temperature: - 40 °C + 70 °C |
| | IEC 60068-2-2 | Operation temperature: - 20 °C + 55 °C |
| | IEC 60068-2-14 | |
| Humidity | IEC 60255-1 | Humidity: 10 % 93 % |
| | IEC 60068-2-78 | |
| | IEC 60068-2-30 | |
| Enclosure protection | IEC 60529 | IP41 from front side, IP2x from rear side |
| | | IP54 Rated mounting kit |
| Mechanical Environment | | |
| Vibration | IEC 60255-21-1 | Class I |
| Shock and bump | IEC 60255-21-2 | Class I |
| Seismic | IEC 60255-21-3 | Class I |
| Electrical Environment | | |
| Dielectric withstand | IEC 60255-27 | Test levels: 2 kV AC 50 Hz (0.705 kV DC for transducer inputs) |
| High voltage impulse | IEC 60255-27 | Test levels: 5 kV (1 kV for transducer and temperature measuring inputs) |
| Insulation resistance | IEC 60255-27 | Insulation resistance > 15 GΩ |
| Voltage dips, interruptions, variations and ripple on dc supply | IEC 60255-26 | Voltage dips: 40 % (200 ms), 70 % (500ms), 80 % (5000 ms) |
| Thermal short time | IEC 60255-27 | |
| Electromagnetic Environment | | |
| Electrostatic discharge | IEC 61000-4-2 | Test voltages: 15 kV air discharge, 8 kV |
| | IEC 60255-26 | contact discharge |
| Radiated radio frequency electromagnetic | IEC 61000-4-3 | Test field strength: 10 V/m |
| field immunity | IEC 60255-26 | |
| Electrical fast transient | IEC 61000-4-4 | Test voltage: 4 kV, 5kHz |
| | IEC 60255-26 | |
| Surge immunity | IEC 61000-4-5 | Test voltages: 4 kV line-to-earth, 2 kV line- |
| | IEC 60255-26 | to-line |
| Immunity to conducted disturbances, | IEC 61000-4-6 | Frequency sweep: 150kHz80 MHz |
| induced by radio-frequency fields | IEC 60255-26 | Spot frequencies: 27 MHz, 68 MHz |
| | | Test voltage: 10 V |
| Power frequency magnetic field | IEC 61000-4-8 | Test field field strength: 100 A/m continuous, |
| immunity | IEC 60255-26 | 1000 A/m for 3 s |
| Damped oscillatory wave immunity | IEC 61000-4-18 | Test frequency: 100 kHz, 1 MHz |
| | IEC 60255-26 | Test voltage: 2.5 kV in common mode, 1 kV in differential mode |



DIMENSION AND PANEL CUT-OUT

Flush mounting of 24HP panel instrument case





PANEL CUT-DUT







S24 Series flush mounting method



Din rail mounting of 24HP panel instrument case





S24 Series din rail mounting method

HARDWARE CONFIGURATION

I/O configuration

The standard number of inputs and outputs of each variant are listed in the table below.

ф ф ф ф ф ф

| Hardware configuration | Var. 0 | Var. 1 | Var. 2 | Var. 6 |
|---|--------|--------|--------|--------|
| Current inputs (4th channel can be sensitive) | | 4 | 4 | |
| Voltage inputs | | | 4 | 4 |
| Binary inputs (BI) | 6* | 6* | 6* | 6* |
| Binary outputs (BO) | 7* | 5* | 5* | 5* |
| IRF(Internal fault relay) contact | 1 | 1 | 1 | 1 |
| Fast trip outputs | | 2* | 2* | 2* |

*: as standard I/O card hardware configuration. The number of binary inputs, binary outputs, and fast trip outputs can be selected as optional (I/O card option)

The maximum number of inputs and outputs of each variant are listed in the table below.

| Hardware configuration | Var. 0 | Var. 1 | Var. 2 | Var. 6 |
|---|--------|--------|--------|--------|
| Current inputs (4th channel can be sensitive) | | 4 | 4 | |
| Voltage inputs | | | 4 | 4 |
| Binary inputs (BI) | 63 | 48 | 33 | 48 |
| Binary outputs (BO) | 48 | 36 | 24 | 36 |
| IRF(Internal fault relay) contact | 1 | 1 | 1 | 1 |
| Fast trip outputs | | 2 | 2 | 2 |

Module arrangement

S24 – Variant 0

| Slot: A | Slot: B | Slot: C | Slot: D | Slot: E | Slot: F |
|---------------|---------------|---------|---------|---------|--------------|
| PSR2+ 2101 | 06R5+ 2101 | | | | CPU+ 0007 |
| | | | | | |

S24 Variant 0 default hardware layout



S24 – Variant 1



S24 Variant 1 default hardware layout

S24 – Variant 2



S24 Variant 2 default hardware layout



S24 – Variant 6



S24 Variant 6 default hardware layout

I/O module options

| IO card type | Slot A | Slot B | Slot C | Slot E | Slot E |
|--------------|--------|--------|----------|--------|--------|
| PS | Option | N/A | N/A | N/A | N/A |
| PSTO | Option | N/A | N/A | N/A | N/A |
| PSTP | Option | N/A | N/A | N/A | N/A |
| PSR2 | Option | N/A | N/A | N/A | N/A |
| O6R5 | N/A | Option | Standard | Option | Option |
| O15 | N/A | Option | Option | Option | Option |
| O12 | N/A | Option | Option | Option | Option |
| O8 | N/A | Option | Option | Option | Option |
| R12 | N/A | Option | Option | Option | Option |
| R8 | N/A | Option | Option | Option | Option |

I/O module options

I/O module types

 $\left[0 \right]$

| PS | PSTO | PSTP | PSR2 | O6R5 | O15 | O12 | 08 | R12 | R8 |
|-------|-----------|-------|-------|---|--|---|--|----------|--|
| (IRF) | (3BI/1BO) | (2BO) | (2BO) | (6BI/5BO) | (15BI) | (12BI) | (8BI) | (12BO) | (8BI) |
| PS+ | PSTO+ | PSTP+ | PSR2+ | O6R5+ | 015+ | 015+ | 08+ | R12+ | R8+ |
| 2101 | 2101 | 2101 | 2101 | 2101 | 1101T | 1101T | 2201 | 0000 | 00 |
| | | | | 는 씨 의 러 이 이 너 이 이 이 이 이 이 이 이 이 이 이 이 이 이 이 이 | ╔╡ ╔┊╘╔╎╕╡╘╓╝┙┤╝╓ ┡┶┝┝┶ <mark>┶</mark> ╪╍╪╍╪╍╪╍╪╍╪╍╪╍╪╍╪╍╪╍╪ | <u>~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ ~ </u> | 1 2 2 3 4 5 6 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 | <u> </u> | <u>ॾॾॾॾॾॾॿॿॴॴॺॷॴ</u> ॸॖऀऻऀऀऀ॔ऻऀऀ॔ऻऀऀ॔ऻऀऀ॔ऻऀऀ॔ऻऀऀ॔ऻऀऀ॔ |

I/O module types

EXTERNAL CONNECTION DIAGRAM

S24 – Variant 0

-XX



0 0

Typical connection diagram for the S24 - Variant 0



1



ä...







T



X

۵.,

Typical connection diagram for the S24 – Variant 2





S24 – Variant 6



ä...

Typical connection diagram for the S24 - Variant 6



CONTACT

For more information, please refer to the Europrot+ Smart Line S24 Series configuration description document or contact us: Protecta Electronics Ltd.

Address: Késmárk u. 7/A, 1158 Budapest, Hungary Mailing Address: 1601 Budapest, Pf. 74., Hungary Phone Number: +36 1 415 3800 E-Mail: protecta@protecta.hu